University of Mumbai

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विद्याविषयक प्राधिकरणे
सभा आणि सेवा विभाग(ए.ए.एम.एस)
रूम नं. १२८ एम.जी.रोड, फोर्ट,
मुंबई – ४०० ०३२
टेलिफोन नं. – ०२२ – ६८३२००३३

(नॅक पुनर्मूल्यांकनाद्वारे ३.६५ (सी.जी.पी.ए.) सह अ++ श्रेणी
विद्यापीठ अनुदान आयोगाद्वारे श्रेणी १ विद्यापीठ दर्जा)

क्र.वि.प्रा.स.से./आयसीडी/२०२५-२६/३७ दिनांक : २७ मे, २०२५

परिपत्रक:-

सर्व प्राचार्य/संचालक, संलग्नित महाविद्यालये/संस्था, विद्यापीठ शैक्षणिक विभागांचे संचालक/ विभाग प्रमुख यांना कळविण्यात येते की, राष्ट्रीय शैक्षणिक धोरण २०२० च्या अमंलबजावणीच्या अनुषंगाने **शैक्षणिक वर्ष २०२५-२६** पासून **पदवी व पदव्युत्तर अभ्यासक्रम** विद्यापरिषदेच्या दिनांक २८ मार्च २०२५ व २० मे, २०२५ च्या बैठकीमध्ये मंजूर झालेले सर्व अभ्यासक्रम मुंबई विद्यापीठाच्या www.mu.ac.in या संकेत स्थळावर NEP २०२० या टॅब वर उपलब्ध करण्यात आलेले आहेत.

मुंबई – ४०० ०३२
२७ मे, २०२५

(डॉ. प्रसाद कारंडे)
कुलसचिव

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AC– 20/5/2025
Item No.– 6.55 (N)

As Per NEP 2020

University of Mumbai

Syllabus for Major Vertical – 1, 4, 5 & 6		
Name of the Programme –B.E. (Electronics and Computer Engineering)		
Faculty of Engineering		
Board of Studies in Electronics Engineering		
U.G. Second Year Programme	**Exit Degree**	**U.G. Diploma in Electronics and Computer Engineering.**
Semester		**III**
From the Academic Year		**2025-26**

University of Mumbai

(As per NEP 2020)

Sr. No.	Heading	Particulars
1	**Title of program** **O:_____**	**B.E. (Electronics and Computer Engineering)**
2	**Exit Degree**	**U.G. Diploma in Electronics and Computer Engineering**
3	**Scheme of Examination** **R: _____**	NEP 40% Internal 60% External, Semester End Examination Individual Passing in Internal and External Examination
4	**Standards of Passing** **R: _____**	**40%**
5	**Credit Structure** **R. TEU-705C** **R. TEU-705D**	**Attached herewith**
6	**Semesters**	**Sem. III**
7	**Program Academic Level**	**5.00**
8	**Pattern**	**Semester**
9	**Status**	**New**
10	**To be implemented from Academic Year**	**2025-26**

Sd/-	**Sd/-**	**Sd/-**
Dr. R.N.Awale	**Dr. Deven Shah**	**Prof. Shivram S. Garje**
BoS-Chairman-Electronics Engineering	**Associate Dean**	**Dean**
Faculty of Technology	**Faculty of Science & Technology**	**Faculty of Science & Technology**

Preamble

To meet the challenge of ensuring excellence and NEP 2020 policy in engineering education, the issue of quality needs to be addressed, debated, and taken forward systematically. Accreditation is the principal means of quality assurance in higher education. The major emphasis of the accreditation process is to measure the outcomes of the program that is being accredited. In line with this Faculty of Science and Technology (in particular Engineering) of the University of Mumbai has taken the lead in incorporating the philosophy of NEP 2020 education in the process of curriculum development.

The second-year engineering course is a core training program to impart scientific and logical thinking training to learners in general, with a choice of course selection from the program core course, multidisciplinary minor, and vocational skill-enhanced course. Simultaneously, the objectives of NEP 2020 demand nurturing the core program and skills required for the Information Technology Branch of engineering in the learner. Keeping this in view, a pool of courses is offered in Core Courses covering fundamentals required to understand core and modern engineering practices and emerging trends in technology. Considering the shift in pedagogy and the convenience of a stress-free learning process, a choice-based subject pool is offered in the coursework under the heads of Information Technology in Engineering for open electives and multidisciplinary minor courses in the third and fourth semesters. Essentially, to give a glimpse of trends in the industry under vocational and enhanced skill practices, the pool is offered to nurture and develop creative skills in contemporary industrial practices. Criteria met in the structure is the opportunity for learners to choose the course of their interest in all disciplines.

Program Core Course Cover Electronics and Computer Engineering based core courses. The technical courses include Open Electives (OE) and Multidisciplinary (MDM)where a pool of subjects is given for selection. Considering the present scenario, diverse choices need to be made available to fulfill the expectation of a learner to aspire for a career in the field of current trends of Technology and interdisciplinary research. Ability enhancement can be achieved in Undergraduate training by giving an objective viewpoint to the learning process and transitioning a learner from a rote learner to a creative professional. for the purpose Design Thinking is introduced in the First Semester to orient a journey learner to become a skilled professional. Considering the NEP-2020 structure of award of Certificate & Diploma at multiple exit-point pools of Vocational skills is arranged for giving exposure to the current Industry practices.

The faculty resolved that course objectives and course outcomes are to be clearly defined for every course so that all faculty members in affiliated higher education institutes understand the depth and approach of the course to be taught, which will enhance the learner's learning process. NEP 2020 grading system enables a much-required shift in focus from teacher-centric to continuous-based learner-centric education since the workload estimated is based on the investment of time in learning and not in teaching. It also focuses on continuous evaluation, which will enhance the quality of education. Credit assignment for courses is based on a 15-week teaching-learning process for NEP 2020, however, the content of courses is to be taught in 12-13 weeks, and the remaining 2-3 weeks are to be utilized for revision, tutorial, guest lectures, coverage of content beyond the syllabus, etc.

There was a concern that in the present system, the second-year syllabus must not be heavily loaded to the learner and it is of utmost importance that the learner entering the second year of an engineering course should feel at ease by lowering the burden of syllabus and credits. This is necessary for a learner to get accustomed to the new environment of a college and to create a bond between the teacher and the learner. The present curriculum will be implemented for the Second Year of Engineering from the academic year 2054-26. Subsequently, this system will be carried forward for Third Year and Final Year Engineering in the academic years 2026-27, and 2027-28, respectively.

Sd/-Sd/-Sd/-
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Faculty of Technology

Dr. Deven Shah
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Prof. Shivram S. Garje
Dean
Faculty of Science & Technology

UnderGraduateDiploma in Engineering- Electronics Engineering

Credit Structure (Sem. III & IV)

	R. TEU-705C								
Level	**Semester**	**Major**		**Minor**	**OE**	**VSC, SEC (VSEC)**	**AEC, VEC, IKS**	**OJT, FP,CEP, CC,RP**	**Cum.Cr. / Sem.**	**Degree/Cum.Cr.**
		Mandatory	**Electives**							
5.0	III	PCC301:3 PCC302:3 PCC303:3 PCC304:3 PCL301: 1 PCL302:1	--	--	OE:2	--	VEC:2 HSL: 2	CEP:2	22	**UG Diploma45**
	R. TEU-705D									
	IV	PCC401:3 PCC402:3 PCC403:3 PCL401:1 PCL402:1	--	MDM: 4	OE:2	VSEC:2	VEC:2 EEM:2	--	23	
	CumCr.	25	--	4	4	2	*2+2+2+2*	2	45	

Exit option: Award of UG Diploma in Major and MDM with 90credits and additional 4credits core **one** theory subject with 3 credits and **one** lab with 1 credit from one third year from where they want to take Exit degree. Along with theory and practical course student must compulsorily do internship for **one month or 160 hours** which internship is equal to 4 credits.

[Abbreviation - OE – Open Electives, VSC – Vocation Skill Course, SEC – Skill Enhancement Course, (VSEC), AEC – Ability Enhancement Course, VEC – Value Education Course, IKS – Indian Knowledge System, OJT – on Job Training, FP – Field Project, CEP – Continuing Education Program, CC – Co-Curricular, RP – Research Project]

SEM. – III
&
SEM-IV

S.E. (Electronics and Computer Engineering)

Scheme

Program Structure for Second Year of Electronics and Computer Science

UNIVERSITY OF MUMBAI (With Effect from 2025-2026)

SEMESTER III

Course Code	Course Description	Teaching Scheme (Contact Hours)			Credit Assigned			
		Theory	Practical	Tutorial	Theory	Tutorial	Practical	Total Credits
2273111	Signals and Systems (Mathematics-III)	2	--	1	2	1	--	3
2273112	Electronic Devices and Circuits	3	–	--	3	--	--	3
2273113	Computer Organization and Architecture	3	--	--	3	--	--	3
2273114	Data Structures and Algorithms	2	--	--	2	--	--	2
OEC301	Open Elective	2#	--	--	2	--	--	2
2273115	Electronic Devices Lab	--	2	--	--	--	1	1
2273116	Data Structures and Algorithms Lab.	--	2	--	--	--	1	1
2273117	Computer Organization and Architecture Lab	--	2	--	--	--	1	1
2273611	Mini Project (group project)	--	2*+2	--	--	--	2	2
2993511	Entrepreneurship Development	--	2*+2	---	--	--	2	2
2993512	Environmental Science for Engineers	--	2*+2	--	--	--	2	2
Total		**12**	**16**	**01**	**12**	**01**	**09**	**22**

* Two hours of practical class to be conducted for full class as demo/discussion.

Theory / Tutorial 1 credit for 1 hour and Practical 1 credit for 2 hours

Institute shall offer a course for Open Elective from Science/Commerce/Management stream bucket provided by the University of Mumbai.

#Institute shall offer a course for MDM from other Engineering Boards.

Course Code	Course Description	Examination scheme							
		Internal Assessment Test (IAT)			End Sem. Exam Marks	End Sem. Exam Duration (Hrs)	Term Work (Tw)	Oral & Pract.	Total
		IAT-I	IAT-II	Total (IAT-I) + IAT-II)					
2273111	Signals and Systems (Mathematics-III)	20	20	40	60	2	25	--	125
2273112	Electronic Devices	20	20	40	60	2	--	--	100
2273113	Computer Organization and Architecture	20	20	40	60	2	--	--	100
2273114	Data Structures and Algorithms	20	20	40	60	2	--	--	100
OEC301	Open Elective	20	20	40	60	2	--	--	100
2273115	Electronic Devices Lab	--	--	--	--	--	25	25	50
2273116	Data Structures and Algorithms Lab.	--	--	--	--	--	25	25	50
2273117	Computer Organization and Architecture Lab						25	25	50
2273611	Mini Project (group project)	--	--	--	--	--	25	25	50
2993511	Entrepreneurship Development	--	--	--	--	--	50	--	50
2993512	Environmental Science for Engineers	--	--	--	--	--	50	--	50
Total		**100**	**100**	**200**	**300**	**10**	**225**	**100**	**825**

Vertical –1
Major

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
2273111	Engineering Mathematics-III	L	T	P	L	T	P	Total
		2	1	--	2	1	-	3
		Examination Scheme						
			IA1	IA2	ESE		Total	
		Theory	20	20	60		100	

Course Objectives:

1. To build a strong foundation in mathematics, provide students with the mathematics fundamentals necessary to formulate, solve and analyze complex engineering problems.
2. To prepare the students to apply reasoning informed by contextual knowledge to engineering practice, and to work as part of teams on multi-disciplinary projects.

Pre-requisite Course Codes	BSC101-Applied Mathematics-I, BSC102-Applied Mathematics-II	
Course Outcomes	After the successful completion, students should be able to	
	CO1	Understand the concept of Laplace transform and its application to solve the real integrals in engineering problems.
	CO2	Understand the concept of inverse Laplace transform of various functions and its applications in engineering problems.
	CO3	Expand the periodic function by using Fourier series for real life problems and complex engineering problems.
	CO4	Apply the concept of vector spaces and orthogonalization process in Engineering Problems
	CO5	Apply the concepts Linear transformations in image processing.
	CO6	Apply the concepts of Eigen values and Eigen vectors to concepts of PCA and image processing.

Module No.	Topics	References	No. of Hours
01	**Laplace Transforms:** 1.1 Definition of Laplace transform, Condition of Existence of Laplace transform. 1.2 Laplace Transform (L) of Standard Functions like e^{at}, sin(at), cos(at), sinh(at), cosh(at) and t^n, $n \geq 0$. 1.3 Properties of Laplace Transform: Linearity, First Shifting theorem, change of scale Property, multiplication by t, Division by t, Laplace Transform of derivatives and integrals (Properties without proof). 1.4 Evaluation of integrals by using Laplace Transformation.	[1], [3]	5
02	**Inverse Laplace Transform:** 2.1 Inverse Laplace Transform, Linearity property, use of standard formulae to find inverse Laplace Transform, finding Inverse Laplace transform using derivatives. 2.2 Partial fractions method to find inverse Laplace transform. 2.3 Inverse Laplace transform using Convolution theorem (without proof).	[1], [3]	4

03	**Fourier Series:** 3.1 Dirichlet's conditions, Definition of Fourier series and Parseval's Identity (without proof). 3.2 Fourier Series on interval (c, c+2l). 3.3 Half range Sine and Cosine Series.	[1], [3]	5
04	**Vectors spaces**: 4.1 Vectors spaces in N dimensional, Finite dimensional Vector spaces, Linear Span, Basis, dimension, Subspace, Cauchy Schwartz Inequality 4.2 Inner Product spaces, Norm, Orthogonal Vectors, Orthogonal Projection and Orthogonal Complements, Gram Schmidt Orthogonalization Process	[2], [4]	4
05	**Linear Transformation:** 5.1 Linear Transformation, types of linear operators (Reflection Projection, Rotation, Contraction, Dialtion, shear), Kernel & Range of Linear Transformation, Rank Nullity Theorem (without proof) 5.2	[2], [4]	4
	Matrix of a linear Transformation, Composition of Liner Transformation and Inverse of liner transformation 5.3. Effect of Change of Bases on Linear Operators		
06	**Matrix: Eigen values & Eigen vectors:** 6.1 Characteristic equation, Eigen values and Eigen vectors, Example based on properties of Eigen values and Eigen vectors. (Without Proof). 6.2. Similarity of Matrices, Diagonalization of Matrices and Functions of Square matrices	[2], [4]	4
			26

Reference Books:

1: Integral Transforms and their Applications by Lokenath Debnath and DambaruBhatta ,Chapam& Hall/CRC
2: An introduction to Integral Transforms by BaidyanathPatra , CRC Press.
3. Advanced engineering mathematics, H.K. Das, S . Chand, Publications
4 Higher Engineering Mathematics, B. V. Ramana, Tata Mc-Graw Hill Publication
5 Advanced Engineering Mathematics, R. K. Jain and S. R. K. Iyengar, Narosa publication
6. Advanced Engineering Mathematics, Wylie and Barret, Tata Mc-Graw Hill.
7. Introduction to Linear Algebra by Gilbert Strang, Wellesly Cambridge Press.
8. Linear Algebra, F. Stephen Friedberg, Arnold Insel, Lawrence Spence, Prentice Hall of India.

Term Work:

General Instructions:

1. Batch wise tutorials are to be conducted. The number of students per batch should be as per University pattern for practicals.
2. Students must be encouraged to write 6 class tutorials on entire syllabus.

Tutorial Guidelines:

Tutorial should be conducted batch wise. Tutorial work will be graded from 20 marks .

Distribution of Term work Marks

1	Attendance	5
2	Class tutorials	20

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
		L	T	P	L	T	P	Total
		2	--	--	2	--	1	3
2273112	Electronic Devices and Circuits	Examination Scheme						
			IA1	IA2	ESE		Total	
		Theory	20	20	60		100	

Pre-requisite Course Codes	ESC 102 BEEE, BSC2022-Semiconductor Physics	
Course Outcomes	After the successful completion students should be able to	
	CO1	Demonstrate Semiconductor applications as clipper and clamper
	CO2	Demonstrate simple rectifiers and filters using PN junction diodes
	CO3	Analyze electronic circuits using BJT, its DC and AC analysis
	CO4	Analyze electronic Circuits using MOSFET, its DC and AC analysis
	CO5	Demonstrate power amplifiers
	CO6	Demonstrate different power electronic devices

Module No.	Unit No.	Topics	Reference	Hrs.
Module 1	**1**	**Clippers & Clampers**		6Hrs
	1.1	Theoretical description of basic structure & construction of p n junction diode, symbol, operation under zero bias, forward bias & reverse bias, avalanche breakdown, V-I characteristics & temperature effects (no mathematical analysis or numerical examples).	1,2	
	1.2	Application of P-N junction diode as clippers & clampers (different types of configurations with input-output waveforms & transfer characteristics; theoretical description & analysis of each circuit; numerical examples)		
Module 2	**2**	**Rectifiers & Filters**		6Hrs
	2.1	Rectifiers: Working & mathematical analysis of full – wave center tapped rectifier & bridge type rectifier (mathematical analysis include expressions for the DC / average & RMS output voltage, DC / average & RMS output current & ripple factor; numerical examples included)	1,2	
	2.2	Filters: Capacitor (C), Inductor (L), Inductor – Capacitor (LC), C-L-C (π) with circuit diagram, waveforms, working / operation & expression for ripple factor (theoretical description only – no analysis or numerical examples to be included)		

Module 3	**3**	**Bipolar Junction Transistor Based Circuits**	1,2 3, 4	8 Hrs
	3.1	DC Circuit Analysis: DC load line and region of operation, common bipolar transistor configurations, biasing circuits, bias stability and compensation, analysis and design of biasing circuits.		
	3.2	AC Analysis of BJT Amplifiers: AC load line, small signal models (h-parameter model, Hybrid-pi model), graphical analysis, ac equivalent circuits and analysis to obtain voltage gain, current gain, input impedance, output impedance of CE, CB and CC amplifiers.		
	3.3	Design of CE Amplifier		
Module 4	**4**	**MOSFET Based Circuits**		8 Hrs
	4.1	DC Circuit Analysis: DC load line and region of operation, common-MOSFETs configurations, analysis and design of biasing circuits	1,2 ,3,4	
	4.2	AC Analysis: AC load line, small-signal model of MOSFET at high and low frequency and its equivalent circuit, small-signal analysis of MOSFET amplifiers, common-source, source follower, common gate.		
	4.3	Design of CS Amplifier using MOSFETS		
Module 5	**5**	**Power Amplifier**		5Hrs
	5.1	Introduction to power amplifiers, difference between voltage and power amplifiers.	2,5,6 ,7	
	5.2	Classification of Class A, Class-B, Class- AB, Class-C power amplifiers, power amplifier using MOSFET		
Module 6	**6**	**Power Electronic Devices**		6Hrs
	6.1	Introduction to power electronic devices and its needs.	9,10	
	6.2	Introduction, scope and application, construction and characteristics of thyristors, power MOSFET, IGBT, IGCT and GTO,		
	6.3	Applications of power electronic devices		
			Total	**39**

Course Assessment:

Theory:

IA1:20 Marks written examination for one hour
IA2:20 Marks written examination for one hour

ESE:60 Marks written examination for two hours

Recommended Books:

[1] Donald A. Neamen, "Electronic Circuit Analysis and Design", TATA McGraw Hill, 2nd Edition
[2] Boylestead," Electronic Devices and Circuit Theory", Pearson Education
[3] James Morris & Krzysztof Iniewski, Nano-electronic Device Applications Handbook by CRC Press
[4] David A. Bell, "Electronic Devices and Circuits", Oxford, Fifth Edition.
[5] Muhammad H. Rashid, "Microelectronics Circuits Analysis and Design", Cengage
[6] S. Salivahanan, N. Suresh Kumar, "Electronic Devices and Circuits", Tata McGraw Hill

[7] Millman and Halkies, “Integrated Electronics”, Tata McGraw Hill.
[8] Adel S. Sedra, Kenneth C. Smith and Arun N Chandorkar, “Microelectronic Circuits Theory and Applications”, International Version, OXFORD International Students Edition, Fifth Edition.
[9] Muhammad H. Rashid, “Power Electronics - circuits, devices and applications”, Prentice Hall of India, 2nd edition.
[10] P. S. Bimbhra, "Power Electronics", Khanna Publishers, New Delhi.

Online References:

NPTEL courses on microelectronics: Devices to circuits

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
2274113	Computer Organization and Architecture	L	T	P	L	T	P	Total
		2	--	--	2	--	1	3
		Examination Scheme						
			IA1	IA2	ESE		Total	
		Theory	20	20	60		100	

Pre-requisite Course Codes: PCC201X		
Course Outcomes	After the successful completion students should be able to:	
	CO1	Evaluate the various types of data representation used in Computing systems.
	CO2	Analyse the design considerations in various units of the Processor.
	CO3	Explain concepts related to cache memory and Virtual memory management in Computer systems
	CO4	Contrast different types of I/O data transfers and I/O buses used in Computer systems
	CO5	Evaluate the advantages and limitations of Parallelism in systems.
	CO6	Explain the architectural enhancements in modern processors

Module No.	Unit No.	Topics	References	Hrs.
1	**Introductory Concepts**			6
	1.1.	Basic Building blocks of a Computer, Moore’s law, Evolution of x86 Computers, Von Neumann model, Harvard Model, Performance measures	1,2,4	
	1.2	Number representation: Floating-point representation, Floating point arithmetic, IEEE 754 floating point number representation	1,2,4	
	1.3	Booth’s Multiplier, Restoring and Non-Restoring Division	1	
2	**Processor Organization**			8
	2.1	Instruction format, Instruction cycle, Instruction set types, Addressing Modes	1,2,4	
	2.2	Datapath Organization (including Control sequences)	1,2,4	
	2.3	Control Unit Design: Hardwired and Microprogrammed, Nano-programming	1,2,4	

	2.4	CISC vs RISC: Design philosophy and issues	1,2,4	
3	**Memory Organization**			**8**
	3.1	Types of memories, Performance parameters of a Memory system, Memory Hierarchy, Memory Interleaving	1,2,4	
	3.2	Cache memory concepts: Principles of locality of Reference, Cache mapping techniques, Cache architectures, Cache coherency (Brief Discussion on MESI model)	1,2,4	
	3.3	Virtual management concepts: Paging, Segmentation, Page Replacement policies	1,2,4	
	3.4	Case Study: Virtual Memory management in Pentium processor	1,2,6	
4	**I/O Organization**			**4**
	4.1	I/O interfacing: Handshaking, Interrupt handling, Direct memory Access (DMA)	1	
	4.2	I/O Buses: Protocols, Arbitration	1	
5	**Parallel processing**			**6**
	5.1	Introduction to Parallel processing, Flynn's Classification, Amdahl's Law	3,4	
	5.2	Pipelining, Pipeline Performance metrics, Pipeline Hazards and Solutions	2,3,4	
6	**Enhancements of Advanced Processor Architectures**			**7**
	6.1	Superscalar processors, Branch Prediction logic, GPUs, Clusters, Multi-core processors	1,4,7	
	6.2	NVIDIA GPU Case study and Programming Model	8	
			Total	**39**

Course Assessment:

Theory:

IA1: One hours 20 Marks written examination for one hour
IA2: One hours 20 Marks written examination for one hour

ESE: Two hours 60 Marks written examination for two hours

Recommended Books:

[1] Carl Hamacher, Zvonko Vranesic, Safwat Zaky, "Computer Organization", Tata Mc Graw-Hill, 5 th Edition.

[2] William Stallings," Computer Organization and Architecture: Designing for Performance", Pearson, 8 th Edition.

[3] Andrew S. Tanenbaum," Structured Computer Organization", Pearson, 6 th Edition.

[4] D. A. Patterson and J. L. Hennessy, "Computer Organization and Design – A Quantitative Approach ", Morgan Kaufmann, 6th Edition.

[5] B. Govindarajulu," Computer Architecture and Organization: Design Principles and Applications", McGraw Hill, 2 nd Edition.

[6] Don Anderson, Tom Shanley, "Pentium Processor System Architecture", Addison Wesley Professional, 2nd Edition.

[7] Douglas V Hall," Microprocessor and Interfacing: Programming & Hardware", Tata-Mc Graw Hill, 3 rd Edition.

[8] Jason Sanders, Edward Kandrot**, "**CUDA by Example: An Introduction to General-Purpose GPU Programming", Addison-Wesley, 1st Edition.

Course Code	Course Name	Teaching Scheme (Hrs / week)			Credits Assigned			
		L	T	P	L	T	P	Total
2273114	Data Structures and Algorithms	2	--	2	2	--	1	3
		Examination Scheme						
			IA1	IA2	ESE			Total
		Theory	20	20	60			100

Pre-requisite Course Codes	VSEC102 (C Programming)	
Course Outcomes	At the end of the course student will be able to	
	CO1	Analyze the time and space complexity of algorithms.
	CO2	Apply divide and conquer strategy to solve a problem.
	CO3	Apply greedy strategy to solve optimization problem.
	CO4	Apply dynamic programming strategy to solve optimization problem.
	CO5	Apply backtracking and branch and bound strategies to solve a problem.
	CO6	Apply various string-matching algorithms to solve pattern matching problems

Module No.	Unit No.	Topics	Ref.	Hrs.
1	1.1	**Introduction to Data Structures and Algorithm Complexity:** Introduction to Data Structures, Types of Data Structures – Linear and Nonlinear, Operations on Data Structures, Concept of array, Static arrays vs Dynamic Arrays, structures. Mathematical preliminaries, time complexity and space complexity, worst-case and average-case analyses, use of order notations.	1,2	4
2	2.1	**Stack and Queues:** , Basic Stack Operations, Representation of a Stack using Array, Applications of Stack – Well form-ness of Parenthesis, Infix to Postfix Conversion and Postfix Evaluation. Queue, Operations on Queue, queue-Round Robin Algorithm.	1,2	4
3	3.1	**Linked List:** Introduction, Representation of Linked List, Linked List v/s Array, Types of Linked List - Singly Linked List (SLL), Operations on Singly Linked List: Insertion, Deletion, reversal of SLL, Print SLL. Implementation of Stack and Queue using Singly Linked List. Variation of LL: Circular Linked List, Doubly Linked List.	1,2	6

4	**4.1**	**Trees and Graphs** Introduction, Tree Terminologies, Binary Tree, Types of Binary Tree, Representation of Binary Trees, Binary Tree Traversals, Binary Search Tree Operations on Binary Search Tree, Graph Terminologies, Representation of graph (Adjacency matrix and adjacency list), Graph Traversals – Depth First Search (DFS) and Breadth First Search (BFS)	1,2	6
6	**6.1**	**Sorting and Searching:** Introduction to Searching: Linear search, Binary search, Sorting: Internal VS. External Sorting, Sorting Techniques: Bubble, Insertion, selection, Quick Sort, Merge Sort, Comparison of sorting Techniques based on their complexity. Hashing Techniques, Different Hash functions, Collision & Collision resolution techniques: Linear and Quadratic probing, Double hashing.	1,2	3
7	**7.1**	**Greedy method and Dynamic Programming** General approach and applications, Knapsack Problem, Job Sequencing with Deadlines Minimum Cost Spanning Trees (Kruskal's and Prim's algorithms), All Pair Shortest Path, Traveling Salesman Problem, Flow Shop Scheduling, MultiStage Graph. Longest Common Subsequence	**1,2**	
Total				**26**

Course Assessment:

Theory:

IA1: One hours 20 Marks written examination for one hour
IA2: One hours 20 Marks written examination for one hour

ESE: Two hours 60 Marks written examination for two hours

Recommended Books:

1 T. H. Cormen, C.E. Leiserson, R. L. Rivest, and C. Stein, "Introduction to algorithms", 2nd Edition, PHI Publication 2005.
2 Ellis Horowitz, Sartaj Sahni, S. Rajsekaran. "Fundamentals of computer algorithms" University Press.
3 "Algorithm Design Manual" by Steven S. Skiena
4 Sanjoy Dasgupta, Christos Papadimitriou, Umesh Vazirani, "Algorithms", Tata McGraw-Hill Edition.
5 S. K. Basu, "Design Methods and Analysis of Algorithm", PHI

Online resources

1. https://nptel.ac.in/courses/106/106/106106131/
2. https://www.coursera.org/specializations/algorithms
3. https://www.mooc-list.com/tags/algorithms
4. https://www.youtube.com/watch?v=aGjL7YXI31Q&list=PLEbnTDJUr_IeHYw_sfBOJ6gk5pie0yP-0
5. https://www.geeksforgeeks.org/design-and-analysis-of-algorithms/
6. Algorithm visualization tool https://visualgo.net/
7. Electrode/ HackerRank platform to solve challenging problems

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
2273115	Electronic Devices and Circuits Lab	L	T	P	L	T	P	Total
		--	--	2	--	--	1	1
		Examination Scheme						
		Term work		Orals			Total	
		25		25			50	

Pre-requisite Course Codes	ESL 102 BEEE Lab, BSL2012 Semiconductor Physics Lab	
Laboratory Objectives	1.	To deliver a hands-on approach for studying electronic devices
	2	To comprehend characteristics of electronic devices; thereby understanding their behavior
	3.	To analyze & calculate inherent parameters of electronic devices through experimental approach
	4.	To introduce modern software simulation tools for modeling & simulation of electronic devices
Laboratory Outcomes	After the successful completion students should be able to	
	LO1	Understand and analyze the operation of clippers and clampers in shaping and modifying waveforms.
	LO2	Simulate basic electronic circuits through software simulation
	LO3	Analyze electronic circuits using BJT and FET (DC & AC analysis)
	LO4	Verify the performance of the designed amplifier through theoretical analysis, simulation, and practical implementation
	LO 5	Study of static characteristic of power devices through software simulation

Laboratory Experiments:

Sr. No.	Title of experiment	Hardware /Software	Module	Reference
1.	To perform Clippers and Clampers.	Hardware	1	1,3
2.	To perform Full wave/Bridge rectifier with LC/pi filter.	Hardware	2	1,3
3.	SPICE simulation of Full wave/Bridge rectifier with LC/pi filter.	Software	2	2
4.	Compare different Biasing Circuits of BJT	Hardware/Software	3	1,3

5.	To perform AC, DC, Transient and frequency response of single stage CE amplifiers.	Hardware/Software	3	1,2,3
6	Design CE amplifier for a given specification	Hardware/Software	3	1,2,3
7	Compare different Biasing Circuits of MOSFETS	Hardware/Software	4	1,2,3
8.	To perform AC, DC, Transient and frequency response of single stage CS MOSFET amplifiers.	Hardware/Software	4	1,2,3
9.	Design of CS Amplifier for a given specification	Hardware/Software	4	1,2,3
10.	Study of Power Amplifier	Software	5	1,2
11.	Study of static characteristics of SCR	Software	6	1,2
12.	Study of static characteristic of Triac and Diac	Software	6	1,2

Laboratory Assessment:

Assessment:

Term Work: Term Work shall consist of at least 10 to 12 practicals based on the above list. Also, Term work Journal must include at least 2 assignments.

Term Work Marks: 25 Marks (Total marks) = 15 Marks (Experiment) + 5 Marks (Assignments) + 5 Marks (Attendance)

Practical/ Oral Exam: An Oral examination will be held based on the above syllabus.

Recommended Books:

[1] David A Bell, "Laboratory Manual for Electronic Devices and Circuits", 4th edition, PHI, 2001.

[2] Muhammed H Rashid, "SPICE for circuits and electronics using PSPICE", 2nd edition, PHI, 1995

[3] Mithal. G.K, "Practicals in Basic Electronics", G K Publishers Private Limited, 1997.

Term Work:

At least 10 experiments covering the entire syllabus of PCL 302 (Electronic Devices and circuits Lab) should be set to have well predefined inference and conclusion. This must include **50%** Hardware and **50%** Simulation experiments. The experiments should be student centric and attempts should be made to make the experiments meaningful and interesting. Experiments must be graded from time to time. The grades should be converted into marks as per the Credit and Grading System manual and should be added and averaged. The grading and term work assessment should be done based on this scheme. The final certification and acceptance of term work ensures satisfactory performance of laboratory work and minimum passing marks in term work. Practical and Oral exam will be based on the entire syllabus.

Note:

Suggested List of Experiments is indicative. However, flexibility lies with individual course instructors to design and introduce new, innovative and challenging experiments, (limited to maximum 30% variation to the suggested list) from within the curriculum, so that the fundamentals and applications can be explored to give greater clarity to the students and they can be motivated to think differently.

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
		L	T	P	L	T	P	Total
2273116	Data Structures and Algorithms Laboratory	--	--	2	--	--	1	1
		Examination Scheme						
		Term work		Oral			Total	
		25		25			50	

Pre-requisite Course Codes	PCL201X	
Laboratory Outcomes	After the successful completion students should be able to:-	
	LO 1	To Implement and analyze time and space complexity in sorting
	LO 2	To find minimum and maximum element of an array using divide and conquer strategy
	LO 3	To identify and implement an algorithm to be used in the construction of communication networks
	LO4	Identify and implement an algorithm to be used in disaster management

Module. No.	Exp. No	Name of the experiment
1	**1**	**Sorting:** Implement and analyze time and space complexity of Modified bubble, Insertion and Selection sort to display exam result of students based on their total marks scored.
2	2	**Divide and Conquer:** Implement and analyze time and space complexity of Quick and Merge sort to display records of an employee working in any organization based on their work experience.
	3	**Divide and Conquer: (Any one)** I. Implement and Analyze time and space complexity of multiplying long Integers using divide and conquer strategy. I. Implement and Analyze time and space complexity of finding minimum and maximum element of an array using divide and conquer strategy
3	**4** **5**	**Greedy Strategy**: **(Any 2)** I. Identify and implement an algorithm to be used to solve the challenge faced by airline and shipping companies of maximizing revenue while adhering to weight and space constraints when loading cargo onto airplanes or ships.determine the optimal selection and allocation of cargo items based on their values (revenue) and weights, ensuring efficient use of cargo space. I. Identify and implement an algorithm to be used in the construction of communication networks (telephone or internet networks) where a telecommunication company needs to lay down cables to connect several cities to establish a reliable network infrastructure. The company wants to minimize the cost of laying down cables while ensuring that all cities are connected and there is no redundancy in the network. I. Identify and implement an algorithm to be used by vending machines to

		determine the optimal combination of coins to give as change to customers.
4	6 7	**Dynamic Programming: (Any 2)** I. Identify and implement an algorithm to be used in disaster management and emergency response systems to find the shortest path for emergency vehicles, such as ambulances or fire trucks, to reach affected areas or victims. I. Identify and implement an algorithm to be used to compare DNA /RNA sequences to identify similarities and evolutionary relationships between organisms. I. Identify and implement an algorithm to be used by city planners and urban developers to determine the shortest paths between all pairs of locations, such as residential areas, commercial centers, and public facilities, to improve accessibility, reduce traffic congestion, and enhance urban mobility.
5	**8**	**Backtracking: (Any 1)** I. Implement N queen problem I. Identify and implement an algorithm to be used for coloring regions on a map such that adjacent regions do not have same color.
6	**9**	**String Matching:** Identify and implement an algorithm to be used by search engines to quickly locate documents containing specific keywords or phrases, improving search efficiency and response time.

Laboratory Assessment:

Term Work: Term Work shall consist of at least 10 to 12 practicals based on the above list. Also, Term work Journal must include at least 2 assignments.

Term Work Marks: **25 Marks** (Total marks) = 15 Marks (Experiments) + 5 Marks (Assignments) + 5 Marks (Attendance)

Practical/ Oral Exam: An Oral examination will be held based on the above syllabus

Recommended Books:

1. T. H. Cormen, C.E. Leiserson, R. L. Rivest, and C. Stein, "Introduction to algorithms", 2nd Edition, PHI Publication 2005.
2. Ellis Horowitz, Sartaj Sahni, S. Rajsekaran. "Fundamentals of computer algorithms" University Press.
3. "Algorithm Design Manual" by Steven S. Skiena
4. Sanjoy Dasgupta, Christos Papadimitriou, Umesh Vazirani, "Algorithms", Tata McGraw-Hill Edition.
5. S. K. Basu, "Design Methods and Analysis of Algorithm", PHI

Online resources

2. https://nptel.ac.in/courses/106/106/106106131/
3. https://www.coursera.org/specializations/algorithms
4. https://www.mooc-list.com/tags/algorithms
5. https://www.youtube.com/watch?v=aGjL7YXI31Q&list=PLEbnTDJUr_IeHYw_sfBOJ6gk5pie0yP-0
6. https://www.geeksforgeeks.org/design-and-analysis-of-algorithms/
7. Algorithm visualization tool https://visualgo.net/
8. Electrode/ HackerRank platform to solve challenging problems

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
2273117	Computer Organization and Architecture Laboratory	L	T	P	L	T	P	Total
		--	--	2	--	--	1	1
		Examination Scheme						
		Term work		Oral			Total	
		25		25			50	

Pre-requisite Course Codes: PCL201X		
Laboratory Objectives	To familiarize the learner with the components of a computer	
	To teach the learner the working of the various memory related structures in a computer	
	To familiarize the learner with concepts of pipelining and Branch Prediction	
Laboratory Outcomes	After the successful completion students should be able to: -	
	LO 1	Describe the various parts of a Computer as well as the design considerations
	LO 2	Simulate various memory related concepts like interleaving, cache memory and Virtual memory management like paging
	LO 3	Simulate various pipeline data hazards like RAW, WAR and WAW
	LO4	Simulate the working of architectural enhancements like Branch Prediction Logic

Suggested list of Laboratory Experiments:

Sr. No.	Title of experiment	Module	Reference
1	Study of the various parts of a computer	1	1
2	Booth Multiplication	1	2
3	Implement Restoring and Non-Restoring Division Algorithm	1	2
4	Design a 4-bit parallel adder and 4 bit parallel subtractor (using 7483)	1	3
5	Simulation of Higher and lower order Memory Interleaving	3	2
6	Simulation of the state diagram of MESI Cache consistency model	3	5
7	Implementation of various cache mapping techniques to measure cache hit rate.	3	4
8	Implement various page replacement policies (LRU, FIFO, LFU)	3	4

9	Simulate various data hazards in a pipeline (for a given program segment).	**5**	**4**
10	Simulation of a 4-state dynamic Branch prediction logic (for eg. Of the Pentium processor)	**6**	**5**

Laboratory Assessment:

Term Work: Term Work shall consist of at least 10 to 12 practicals based on the above list. Also, Term work Journal must include at least 2 assignments.

Term Work Marks: **25 Marks** (Total marks) = 15 Marks (Experiments) + 5 Marks (Assignments) + 5 Marks (Attendance)

Practical/ Oral Exam: An Oral examination will be held based on the above syllabus.

Recommended Books:

[1] B. Govindarajulu, "*Computer Architecture and Organization: Design Principles and Applications*", Second Edition, Tata McGraw-Hill.

[2] C. Hamacher, Z. Vranesic and S. Zaky, "Computer Organization", McGraw Hill, 2002.

[3] John F. Wakerly, "Digital Design Principles and Practice"- Pearson Publications, 4th edition

[4] William Stallings, "*Computer Organization and Architecture: Designing for Performance*", Eighth Edition, Pearson.

[5] Don Anderson, Tom Shanley, "Pentium Processor System Architecture", Second Edition, Mindshare INC.

SEC

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
2273611	Mini- Project	L	T	P	L	T	P	Total
		--	--	4	--	--	2	2
		Examination Scheme						
		Term work		Orals			Total	
		25		25			50	

Pre-requisite Course Codes		
Course Outcomes	After the successful completion students should be able to	
	CO1	Identify and address community needs and challenges which help learners to develop problem-solving skills and creativity in finding innovative solutions.
	CO2	Enhance their cultural competence and ability to work effectively in multicultural settings
	CO3	Critically think on complex issues considering multiple view points
	CO4	Demonstrate collaboration, team work, civic engagement, empathy, and compassion while engaging directly with community
	CO5	Develop a lifelong commitment to social justice and making a positive impact in the world

This course requires students to participate in field-based learning/projects generally under the supervision of faculty. The curricular component of 'community engagement and service' involve activities that would expose students to the socio-economic issues in society so that the theoretical learnings can be supplemented by actual life experiences to generate solutions to real-life problems. At the end of the course, it is expected that students will have valuable learnings in terms of enhanced communication skills, increased cultural competence, improved critical thinking, leadership skills, collaboration skills, empathy & compassion, civic engagement, problem-solving skills, self-reflection & personal growth, and long-term commitment to social justice. It is expected that 26-30 hours of contact time per credit in a semester (52 to 60 hours in a semester for 2 credits) along with 13-15 hours of activities such as preparation for community engagement and service, preparation of reports, etc., and independent reading and study.

Other Guidelines to students for successful Community Engagement:

Community engagement is the process of working collaboratively with and through groups of people affiliated by geographic proximity, special interest, or similar situations to address issues affecting the well-being of those people It is a powerful vehicle for bringing about environmental and behavioural changes that will improve the health of the community and its members. It often involves partnerships and coalitions that help mobilize resources and influence systems, change relationships among partners, and serve as catalysts for changing policies, programs, and practices. Community engagement project is different as compared to traditional consultation. It is a regular engagement of community for achieving an identified goal or vision. It recognizes the role of community engagement in its broadest sense in the development of local democracy, while noting that the focus of the report is on the practice of community engagement as it relates to local authority activity. Communication, diplomacy, patience, and flexibility are essential to engage with a community. For successful engagement conditions include: Shared and defined purpose. Willingness to collaborate. Commitment to contributing. Participation of the right people. Open and credible process. Involvement of a champion with credibility and clout. Ensure that the engagement process is complex but

manageable. Initially the team will: Discuss and define the initiative and its potential impact. Set the purpose and goals for community engagement. Define the community. Know and respect the community's characteristics. Develop a relationship with the community, build trust, work with formal and informal leadership, find the community gatekeeper, identify the project champion, meet with the local organizations, and learn the assets and challenges for that community keeping in mind the 17 sustainable development goals. Find the common interests. The following four phases provide broad outline for the community engagement process:

Phase-I: **Outreach**

Go to the community instead of having the community come to you. Invite the stakeholders to a conversation. Create a constructive environment for dialogue allowing time to get to know the participants remembering that the community's time is valuable and must be respected. Identify the person or the organization that has convened the group and will provide initial leadership and organizational management. Outline the purpose and process for the conversation. Use a facilitator when appropriate. Define the issue and why it is important. Outline what is broken and focus on what is working. Is the issue a people problem or a situation problem? Can the problem be solved with technical expertise or will it require something else? Determine the interest and merit in hosting future discussions.

Phase-II: **Gather Facts, Brainstorm and Select**

Create an environment for discussion where people are comfortable asking questions, expressing doubts, and brainstorming new ideas. Gather the facts related to the issue and its impact. Use a SWOT, appreciative inquire, asset mapping, and other tools during the factfinding stage. Clarify the issue's alignment with the community's values and ethics. Establish the common ground on which conversations will be based. Brainstorm and gather alternative solutions. Ask the "what if" questions. Spend time discussing the options and the potential impact. Allow the process to equip the participants to see the change, feel the change, and then be prepared to change. Select the best practice/solution. If required use decision-making tools to reduce the number of options.

Phase-III: **Plan and Review**

Write the implementation action plan. Include the evaluation procedure that will answer the question "What will it look like when the change has happened?". Discuss the proposal with the appropriate stakeholders searching for insight and response. Use the feedback to assess and revise the plan. Stay focused on the solution.

Phase-IV: **Implement and Evaluate**

Implement the plan. Remember, groups want a rapid success. Identify an action that will provide a "meaningful win" within the "immediate reach." Evaluate the impact. Report the status to the community and gather feedback. Revise the plan and evaluate again. Keep the participants informed through discussion agendas, written summaries of previous discussions, goals/assignments for the next discussion, and progress reports providing accountability for delivering what was promised.

Vertical – 5

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2993511	Entrepreneurship Development	2*	2	-	-	-	-	2

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Practical/ Oral	Total
		Internal assessment			End Sem. Exam			
		IAT-I	IAT-II	IAT-I + IAT-II				
2993511	Entrepreneurship Development	--	--	--	--	50	--	50

Note: * Two hours of practical class to be conducted for full class as demo/discussion/theory.

Lab Objectives:

1. To introduce students to entrepreneurship concepts and startup development.
2. To develop business idea generation, validation, and business model preparation.

3. To provide hands-on experience in market research, financial planning, and business pitching.
4. To enhance problem-solving and decision-making skills in entrepreneurial ventures.
5. To familiarize students with government schemes and support systems for entrepreneurs.
6. To develop communication and presentation skills required for business pitching.

Lab Outcomes:

Upon successful completion of this course, students will be able to:

1. Understand the fundamental concepts of entrepreneurship and business models.
2. Conduct market research and develop business plans.
3. Utilize financial planning and cost analysis for startups.
4. Apply entrepreneurial skills to identify and solve business challenges.
5. Develop prototypes using open-source software for business operations.
6. Pitch business ideas effectively with structured presentations.

DETAILED SYLLABUS

Sr. No.	Module	Detailed Content	Hours	LO Mapping
0	**Prerequisite**	Fundamentals of communication and leadership skills.	01	--
I	**Introduction to Entrepreneurship**	Definition, Characteristics, and Types of Entrepreneurs. Entrepreneurial Motivation and Traits. Start-up Ecosystem in India. Challenges in Entrepreneurship	**02**	**LO1**
II	**Business Idea Generation & Validation**	Ideation Techniques: Design Thinking, Brainstorming, Mind Mapping. Business Model Canvas (BMC). Market Research & Customer Validation. Minimum Viable Product (MVP) Concept.	**04**	**LO2**
III	**Business Planning & Strategy**	Writing a Business Plan. SWOT Analysis and Competitive Analysis. Financial Planning and Budgeting. Risk Assessment and Management	**04**	**LO3**
IV	**Funding and Legal Framework**	Sources of Funding: Bootstrapping, Angel Investors, Venture Capital Government Schemes & Start-up India Initiatives. Business Registration & Legal Formalities. Intellectual Property Rights (IPR) & Patents	**05**	**LO4**
V	**Marketing & Digital Presence**	Branding and Digital Marketing. Social Media Marketing & SEO. Customer Relationship Management (CRM). E-commerce & Online Business Models	**05**	**LO5**
VI	**Business Pitching & Prototype Development**	Pitch Deck Preparation & Presentation Techniques. Prototyping with Open-source Tools. Elevator Pitch & Investor Pitch. Case Studies of Successful Start-ups	**05**	**LO6**

Text Books:

1. "Entrepreneurship Development and Small Business Enterprises" – Poornima M. Charantimath, Pearson, 3rd Edition, 2021.
2. "Innovation and Entrepreneurship" – Peter F. Drucker, Harper Business, Reprint Edition, 2019.
3. "Startup and Entrepreneurship: A Practical Guide" – Rajeev Roy, Oxford University Press, 2022.
4. "Essentials of Entrepreneurship and Small Business Management" – Norman Scarborough, Pearson, 9th Edition, 2021.
5. "The Lean Startup" – Eric Ries, Crown Publishing, 2018.

References:

1. "Disciplined Entrepreneurship: 24 Steps to a Successful Startup" – Bill Aulet, MIT Press, 2017.
2. "Zero to One: Notes on Startups, or How to Build the Future" – Peter Thiel, 2014.
3. "The $100 Startup" – Chris Guillebeau, Crown Business, 2019.
4. "Business Model Generation" – Alexander Osterwalder & Yves Pigneur, Wiley, 2020.
5. "Blue Ocean Strategy" – W. Chan Kim & Renée Mauborgne, Harvard Business Review Press, 2019.

Online Resources:

Website Name
1. Startup India Portal – https://www.startupindia.gov.in 2. MIT OpenCourseWare – Entrepreneurship – https://ocw.mit.edu/courses/sloan-school-of-management/ 3. Coursera – Entrepreneurship Specialization – https://www.coursera.org/specializations/entrepreneurship 4. Harvard Business Review – Entrepreneurship Articles – https://hbr.org/topic/entrepreneurship 5. Udemy – Startup & Business Courses – https://www.udemy.com/courses/business/entrepreneurship/

List of Experiments.

Sr No	List of Experiments	Hrs
01	Business Idea Generation using Mind Mapping.	02
02	Conducting Market Research & Customer Validation.	02
03	Preparing a Business Model Canvas for a Startup Idea.	02
04	Developing a Financial Plan & Break-even Analysis.	02
05	Creating a Website using WordPress/Wix.	02
06	Social Media Marketing Campaign using Open-source Tools.	02
07	Digital Prototyping using Figma/Inkscape.	02
08	Business Pitch Deck Preparation & Presentation.	02
09	Exploring Government Schemes for Startups.	02
10	Legal Compliance & IPR Basics (Case Study).	02

Sr No	List of Assignments / Tutorials	Hrs
01	a. Write a report on any successful entrepreneur and their startup journey. b. Conduct SWOT analysis for a real-life startup.	02
02	Develop a business idea and create a one-page business plan.	02

03	Conduct market research using surveys & present findings.	02
04	Design a simple logo and branding strategy for a startup.	02
05	Create a financial model and cost estimation for a startup.	02
06	Make a case study report on startup failure analysis.	02

List of Open-Source Software

1. Canva – Designing pitch decks, social media posts, and branding materials.
2. Trello / Asana – Project management for startups.
3. GIMP / Inkscape – Graphic design and logo creation.
4. WordPress / Wix – Website development for startups.
5. OpenCart / PrestaShop – E-commerce website setup.
6. Figma – UI/UX design and prototyping.
7. LibreOffice Calc – Financial planning and budgeting.
8. Google Suite (Docs, Sheets, Slides) – Documentation and presentations.
9. Python (Pandas, Flask, Django) – Data analytics and web application development.
10. MailChimp – Email marketing and customer engagement.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2993512	**Environmental Science**	**1**	**-**	**-**	**1**	**-**	**-**	**3**

Assessment :

Term Work: Term Work shall consist of at least 10 practicals' based on the above list. Also, Term work Journal must include at least 6 assignments.

Term Work Marks: 50 Marks (Total marks) = 15 Marks (Experiment) + 15 Marks (Assignments) + 5 Marks (Attendance)+ 10 Marks (Report)

		Theory					Term work	Pract / Oral	Total
		Internal Assessment			End Sem Exam	Exam Duration (in Hrs)			
		Test 1	Test 2	Avg.					
2993512	**Environmental Science**	-	-	-	-	-	25	25	50

Rationale:

Most of the engineering branches are offspring of applied sciences, and their practices have a significant impact on the environment. Understanding environmental studies is essential for engineers to develop sustainable solutions, minimize ecological footprints, and promote responsible resource management. This course equips students with the knowledge of ecosystems, biodiversity, pollution control, and environmental laws, enabling them to integrate sustainability into engineering practices.

Course Objectives:

1. To understand the scope, importance, and role of environmental studies in public awareness and health.
2. To study different natural resources, their issues, and sustainable conservation.

3. To understand ecosystem types, structures, and functions.
4. To explore biodiversity, its importance, threats, and conservation.
5. To learn about pollution types, causes, effects, and control measures.
6. To understand environmental challenges, sustainability, and ethics.

Course Outcomes:

1. Explain the significance of environmental studies and the role of IT in environment and health.
2. Describe resource types, associated problems, and conservation methods.
3. Classify ecosystems and explain their role in ecological balance
4. Analyze biodiversity levels and conservation strategies, especially in India.
5. Explain pollution impacts and suggest preventive measures.
6. Discuss environmental issues and propose sustainable solutions.

DETAILED SYLLABUS:

Unit Name	Topic Name	Topic Description	No of Lecture
Module-I	The Multidisciplinary Nature of Environmental Studies	Definition, scope and importance. Need for public awareness, Role of information technology in environment and human health. Human population and the environment: Population growth, variation among nations. Population Explosion- family welfare program. Environment and human health Women and child welfare	2
Module-II	Natural Resources	Renewable and non-renewable resources. Natural resources & associated problems: a) Forest resources: b) Water resources: Natural resources & associated problems c) Mineral resources: d) Food resources: e) Energy resources: Role of an individual in conservation of natural resources: f) Equitable use of resources for sustainable lifestyles.	2
Module-III	Ecosystems	Concepts of an ecosystem. Introduction, types, characteristic features, structure and function of the following ecosystem: a. Forest ecosystem b. Grassland ecosystem c. Desert ecosystem d. Aquatic ecosystem (ponds, streams, lakes, rivers, oceans, estuaries). Case study on various ecosystems in India.	2
Module-IV	Biodiversity and its Conservation	Introduction-Definition: genetic species and ecosystem diversity. Bio-geographical classification of India Value of biodiversity: Consumptive use, productive use, social, ethical, aesthetic and option values, Bio-diversity at global, national, local levels India as a mega diversity nation Case study on Bio diversity in India.	3

Module-V	Environmental Pollution Definition	Causes, effects and control measures of: a) Air pollution b) Water pollution c) Soil pollution. Solid waste management: Causes, effect and control measures of urban and industrial wastes. Role of an individual in prevention of pollution, Case study on Pollution Disaster management: floods, earthquake, cyclone and landslides. Carbon Credits for pollution prevention	3
Module-VI	Social Issues and Environment	From unsustainable to sustainable development Urban problems related to energy, Water conservation, rain water harvesting, watershed management. Environmental ethics: issues and possible solution. Climate change, global warming, acid rain, ozone layer depletion, nuclear accidents and holocaust. Case studies. Consumerism and waste products. Environment protection act. Public awareness Case study on Environmental Ethics	3

Textbooks

1. Environmental Science: Towards a Sustainable Future, G.Tyler Millerand Scott Spoolman, 13th Edition, Cengage Learning 2021
2. Environmental Management: Text and Cases, Bala Krishnamoorthy, 3rd Edition, PHI Learning, Publication Year: 2016
3. GreenIT: Concepts, Technologies, and Best Practices, Markus Allemann, Springer 2008
4. Sustainable IT:Slimming Downand Greening Up YourIT Infrastructure,DavidF. Linthicum, IBM Press 2009
5. Environmental Modelling: Finding Solutions to Environmental Problems, David L. Murray, Cambridge University Press 2016
6. Remote Sensing and Image Interpretation, Thomas M.Lilles and, Ralph W. Kiefer, and Jonathan W. Chipman, 9th Edition, John Wiley & Sons 2020
7. Business Ethics: Concepts and Cases, Manuel Velasquez, Pearson2012

Reference Books

1. Environmental Lawand Policy in India, Shyam Divan and ArminRosencranz,2nd Edition, Oxford University Press 2018
2. The International Handbook of Environmental Laws, David Freestone and Jonathon L. Rubin, Edward Elgar Publishing 2000
3. E-Waste Management: Challenges and Opportunities in Developing Countries, Ruediger Kuehr and Ram K. Jain, Springer 2008
4. TheE-Waste Handbook: Managing Electronic Waste, Klaus Hieronymi, Ruediger Kuehr, and Ram K. Jain, Earthscan 2009
5. Environmental Ethics: An Introduction ,J.Baird Callicott, University of Georgia Press1999

Online References:

Sr. No.	Website Name
1.	Centre for Science and Environment (CSE), Website: cseindia.org
2.	Ministry of Environment, Forest and Climate Change (MoEFCC), Government of India
3.	CSIR-National Environmental Engineering Research Institute (NEERI)

Assessment:

Internal Assessment (IA) for 20 marks:

- IA will consist of Two Compulsory Internal Assessment Tests. Approximately 40% to 50% of syllabus content must be covered in First IA Test and remaining 40% to 50% of syllabus content must be covered in Second IA Test

- **Question paper format**

  - Question Paper will comprise of a total of **six questions each carrying 20 marksQ.1** will be **compulsory** and should **cover maximum contents of the syllabus**

  - **Remaining questions** will be **mixed in nature** (part (a) and part (b) of each question must be from different modules. For example, if Q.2 has part (a) from Module 3 then part (b) must be from any other Module randomly selected from all the modules)

  - A total of **Three questions** needs to be answered

Vertical – 6

(Open Electives and MDM Courses)

Detailed Syllabus

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
		L	T	P	L	T	P	Total
		2	--	--	2	--	--	2
OE301	**Introduction to IoT and Applications**	**Examination Scheme**						
			IA1	**IA2**	**ESE**		**Total**	
		Theory	20	20	60		100	

Course Objectives:

1. Define the Internet of Things (IoT) and its key characteristics.
2. Explore the conceptual framework and architectural views of IoT systems.
3. Identify the technologies and components that enable IoT implementations.
4. Understand communication protocols and design principles for connected devices.
5. Examine various sensor and actuator technologies used in IoT applications.
6. Apply IoT design methodologies through case studies in smart living and connected commerce.

Course Outcomes	After the successful completion students should be able to	
	CO1	Articulate the fundamental concepts and significance of IoT.
	CO2	Analyze and differentiate between various IoT technologies and protocols.
	CO3	Design and implement basic IoT applications using appropriate sensors and actuators.
	CO4	Evaluate the effectiveness of IoT solutions in real-world scenarios.
	CO5	Conduct case studies to assess the impact of IoT on smart living and commerce.
	CO6	Collaborate on innovative IoT projects, demonstrating practical application of learned concepts.

Module No.	Unit No.	Topics	Reference	Hrs.
1	**Introduction to Internet of Things**		1,2	6
	1.1	Definition and characteristics, IoT conceptual framework		
	1.2	IoT architectural View		
	1.3	Technology behind IoT – server end technology, Major components of IoT system, Development too, ls and Open-source Framework for IoT implementation, APIs and device Interfacing Components, platforms, and Integration tools, M2M		
2	**Design Principles for Connected Devices and Web Connectivity**		1,2	6
	2.1	Overview of NFC, RFID, Bluetooth, Bluetooth LE, Zigbee, Wi-Fi, GSM		
	2.2	Constrained RESTful Environment (CoRE), CoAP, REST, HTTP, HTTPS, and web-sockets		
	2.3	Internet connectivity		
3	**Sensors and Actuators**		1,2	4
	3.1	Sensor technology – Analog and digital sensors, temperature sensor, humidity sensor, distance sensor, light sensor, acceleration sensor		
	3.2	Participatory sensing, Industrial IoT		
	3.3	Actuators – LED, Piezoelectric vibrator , piezoelectric speaker, motor, relay switch		
4	**IoT Platforms Design Methodology**		1,2	4
	4.1	10 step IoT design Methodology		
	4.2	Case study: IoT system for Weather Monitoring		
5	**Case Studies Based on Smart Living**		1,2	4
	5.1	Smart lighting, gas/smoke detection		

	5.2	Smart parking, emergency response		
	5.3	Smart irrigation, wearable electronics for health and fitness monitoring		
6	**Case studies based on Connected Commerce**		1,2	4
	6.1	Inventory management, smart payment		
	6.2	Fleet tracking		
Total				**26**

Course Assessment:

Theory: **IA1:**One hours 20 Marks written examination for one hour

IA2: One hours 20 Marks written examination for one hour

ESE: Two hours 60 Marks written examination for two hours

Reference text books:

1. Internet of Things – Architecture and Design Principles – Raj Kamal
2. Internet of Things – A Hands on Approach – Arshdeep Bahga and Viajy Madisetti

Course Code	**Course Name**	**Teaching Scheme (Hrs/week)**			**Credits Assigned**			
OE401	**Robotics and Its Applications**	L	T	P	L	T	P	Total
		2	--	--	2	--	--	2
		Examination Scheme						
			IA1	**IA2**	**ESE**		**Total**	
		Theory	20	20	60		100	

Course Objectives:

1. To introduce Robotics and discuss the Functional concepts of Robots
2. To explore and learn Configurations of Robots and their Kinematics
3. To introduce path planning techniques for Robotics
4. To explore sensors and understand the concepts of drives and grippers
5. To understand the applications of Robotics
6. To learn about Humanoid Robotics Technology and Social Robots

Course Outcomes	After the successful completion students should be able to	
	CO1	Understand the significance, social impact and future prospects of robotics and automation in various engineering applications.
	CO2	Understand the various configurations and kinematics of robots
	CO3	Know about various path planning techniques
	CO4	Learnt about sensors used in robots along with concepts of drives and grippers
	CO5	Explored the domains of applications for robotics
	CO6	Know about the Humanoid Robotics Technology and Social Robots.

Module No.	**Topics**	**Reference**	**Hrs.**
1	**Introduction:**	T1	4

	Introduction to Robotics, Laws of robot, brief history of robotics, basic components of robot, robot specifications, classification of robots, human system and robotics, safety measures in robotics, social impact, Robotics market, and the future prospects, advantages and disadvantages of robots.	T3 R6	
2	**Configuration and Kinematics** Robot configurations: polar, cylindrical, Cartesian, and jointed arm configurations, Robot links and joints, Degrees of freedom: types of movements, vertical, radial and rotational traverse, roll, pitch and yaw, Wok volume/envelope, Robot kinematics: Introduction to direct and inverse kinematics, transformations and rotation matrix.	T1 T3 R3	4
3	**Sensors** Characteristics of sensing devices, Criterion for selections of sensors, Classification, & applications of sensors. Internal sensors: Position sensors, & Velocity sensors, External sensors Internal sensors: Position sensors, & Velocity sensors, External sensors: Proximity sensors, Tactile Sensors, & Force or Torque sensors	T3 T5 R3	5
4	**Drives and Grippers:** Drives – Basic types of drives. Advantages and Disadvantages of each type. Selection / suitability of drives for Robotic application. Controllers, Types of Controllers, and introduction to close loop controller Grippers – Mechanisms for actuation, Magnetic gripper vacuum cup	T1 T5 R5	5
5	**Robotics Applications:** Material Handling: pick and place, palletizing and depalletizing, machining loading and unloading, welding & assembly, Medical, agricultural and space applications, unmanned vehicles: ground, Ariel and underwater applications, robotic for computer integrated manufacturing. Types of robots: Manipulator, Legged robot, wheeled robot, aerial robots, Industrial robots, Humanoids, Robots, Autonomous robots, and Swarm robots	T1 T3 R6	5
6	**Humanoid Robotics Technology and Social Robots:** Sensors in Humanoid Robot, Control of Humanoid Robot, actuation types for humanoid Robot, System Integration in Humanoid Robot, Social Robot, Need of Social Robots, Assistive and Social Robots in the Healthcare Sector and other, Case study On Humanoid Robot.	T4 T5 R5	5
Total			**26**

Course Assessment:

Theory:

IA1: One hours 20 Marks written examination for one hour
IA2: One hours 20 Marks written examination for one hour

ESE: Two hours 60 Marks written examination for two hours

Reference Books:

1. S. K. Saha, Introduction to Robotics, TATA McGraw Hills Education, 2014.
2. S. B. Nikku, Introduction to Robotics – Analysis, Control, Applications, 3rd edition, John Wiley & Sons Ltd., 2020.
3. Mikell Groover, Mitchell Weiss, Roger N. Nagel, Nicholas Odrey, Ashish Dutta, Industrial Robotics 2nd edition, SIE, McGraw Hill Education (India) Pvt. Ltd., 2012
4. Ganesh S Hegde, "A textbook on Industrial Robotics", University science press,

3rdedition, 2017.
5. S.R. Deb, Robotics Technology and flexible automation, Tata McGraw-Hill Education, 2009.

Text Books:

1. John J. Craig, Introduction to Robotics, Pearson Education Inc., Asia, 3rd Edition, 2005.
2. Asitava Ghoshal, Robotics: Fundamental concepts and analysis, Oxford University Press, 2006.
3. Elmer P. Dadios, "Humanoid Robot: Design and Fuzzy Logic Control Technique for Its Intelligent Behaviors", 2012.
4. Fu K S, Gonzalez R C, Lee C.S.G, "Robotics: Control, Sensing, Vision and Intelligence",McGraw Hill, 1987.

Letter Grades and Grade Points:

Semester GPA/ Programme CGPA Semester/ Programme	% of Marks	Alpha-Sign/ Letter Grade Result	Grading Point
9.00 - 10.00	90.0 – 100	O (Outstanding)	10
8.00 - < 9.00	80.0 - < 90.0	A+ (Excellent)	9
7.00 - < 8.00	70.0 - < 80.0	A (Very Good)	8
6.00 - < 7.00	60.0 - < 70.0	B+ (Good)	7
5.50 - < 6.00	55.0 - < 60.0	B (Above Average)	6
5.00 - < 5.50	50.0 - < 55.0	C (Average)	5
4.00 - < 5.00	40.0 - < 50.0	P (Pass)	4
Below 4.00	Below 40.0	F (Fail)	0
Ab (Absent)	-	Ab (Absent)	0

Sd/-

Dr. R.N.Awale
BoS-Chairman-Electronics Engineering
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Dr. Deven Shah
Associate Dean
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