University of Mumbai

वेबसाईट – mu.ac.in
ईमेल - आयडी - dr.aams@fort.mu.ac.in
aams3@mu.ac.in

विद्याविषयक प्राधिकरणे
सभा आणि सेवा विभाग(ए.ए.एम.एस)
रूम नं. १२८ एम.जी.रोड, फोर्ट,
मुंबई – ४०० ०३२
टेलिफोन नं. – ०२२ – ६८३२००३३

(नॅक पुनर्मूल्यांकनाद्वारे ३.६५ (सी.जी.पी.ए.) सह अ++ श्रेणी
विद्यापीठ अनुदान आयोगाद्वारे श्रेणी १ विद्यापीठ दर्जा)

क्र.वि.प्रा.स.से./आयसीडी/२०२५-२६/३७ दिनांक : २७ मे, २०२५

परिपत्रक:-

सर्व प्राचार्य/संचालक, संलग्नित महाविद्यालये/संस्था, विद्यापीठ शैक्षणिक विभागांचे संचालक/ विभाग प्रमुख यांना कळविण्यात येते की, राष्ट्रीय शैक्षणिक धोरण २०२० च्या अमंलबजावणीच्या अनुषंगाने **शैक्षणिक वर्ष २०२५-२६** पासून **पदवी व पदव्युत्तर अभ्यासक्रम** विद्यापरिषदेच्या दिनांक २८ मार्च २०२५ व २० मे, २०२५ च्या बैठकीमध्ये मंजूर झालेले सर्व अभ्यासक्रम मुंबई विद्यापीठाच्या www.mu.ac.in या संकेत स्थळावर NEP २०२० या टॅब वर उपलब्ध करण्यात आलेले आहेत.

मुंबई – ४०० ०३२
२७ मे, २०२५

(डॉ. प्रसाद कारंडे)
कुलसचिव

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2	The Deputy Registrar, Result unit, Vidyanagari drresults@exam.mu.ac.in
3	The Deputy Registrar, Marks and Certificate Unit,. Vidyanagari dr.verification@mu.ac.in
4	The Deputy Registrar, Appointment Unit, Vidyanagari dr.appointment@exam.mu.ac.in
5	The Deputy Registrar, CAP Unit, Vidyanagari cap.exam@mu.ac.in
6	The Deputy Registrar, College Affiliations & Development Department (CAD), deputyregistrar.uni@gmail.com
7	The Deputy Registrar, PRO, Fort, (Publication Section), Pro@mu.ac.in
8	The Deputy Registrar, Executive Authorities Section (EA) eau120@fort.mu.ac.in He is requested to treat this as action taken report on the concerned resolution adopted by the Academic Council referred to the above circular.
9	The Deputy Registrar, Research Administration & Promotion Cell (RAPC), rapc@mu.ac.in
10	The Deputy Registrar, Academic Appointments & Quality Assurance (AAQA) dy.registrar.tau.fort.mu.ac.in ar.tau@fort.mu.ac.in
11	The Deputy Registrar, College Teachers Approval Unit (CTA), concolsection@gmail.com
12	The Deputy Registrars, Finance & Accounts Section, fort draccounts@fort.mu.ac.in
13	The Deputy Registrar, Election Section, Fort drelection@election.mu.ac.in
14	The Assistant Registrar, Administrative Sub-Campus Thane, thanesubcampus@mu.ac.in
15	The Assistant Registrar, School of Engg. & Applied Sciences, Kalyan, ar.seask@mu.ac.in
16	The Assistant Registrar, Ratnagiri Sub-centre, Ratnagiri, ratnagirisubcentar@gmail.com
17	The Director, Centre for Distance and Online Education (CDOE), Vidyanagari, director@idol.mu.ac.in
18	Director, Innovation, Incubation and Linkages, Dr. Sachin Laddha pinkumanno@gmail.com
19	Director, Department of Lifelong Learning and Extension (DLLE), dlleuniversityofmumbai@gmail.com

Copy for information :-	
1	P.A to Hon'ble Vice-Chancellor, vice-chancellor@mu.ac.in
2	P.A to Pro-Vice-Chancellor pvc@fort.mu.ac.in
3	P.A to Registrar, registrar@fort.mu.ac.in
4	P.A to all Deans of all Faculties
5	P.A to Finance & Account Officers, (F & A.O), camu@accounts.mu.ac.in

To,

1	The Chairman, Board of Deans pvc@fort.mu.ac.in
2	**Faculty of Humanities,** **Offg. Dean** 1. Prof.Anil Singh Dranilsingh129@gmail.com **Offg. Associate Dean** 2. Prof.Manisha Karne mkarne@economics.mu.ac.in 3. Dr.Suchitra Naik Naiksuchitra27@gmail.com
	Faculty of Commerce & Management, **Offg. Dean,** 1 Prin.Ravindra Bambardekar principal@model-college.edu.in **Offg. Associate Dean** 2. Dr.Kavita Laghate kavitalaghate@jbims.mu.ac.in 3. Dr.Ravikant Balkrishna Sangurde Ravikant.s.@somaiya.edu 4. Prin.Kishori Bhagat kishoribhagat@rediffmail.com

	Faculty of Science & Technology **Offg. Dean** 1. Prof. Shivram Garje ssgarje@chem.mu.ac.in **Offg. Associate Dean** 2. Dr. Madhav R. Rajwade Madhavr64@gmail.com 3. Prin. Deven Shah sir.deven@gmail.com
	Faculty of Inter-Disciplinary Studies, **Offg. Dean** 1.Dr. Anil K. Singh aksingh@trcl.org.in **Offg. Associate Dean** 2.Prin.Chadrashekhar Ashok Chakradeo cachakradeo@gmail.com 3. Dr. Kunal Ingle drkunalingle@gmail.com
3	Chairman, Board of Studies,
4	The Director, Board of Examinations and Evaluation, dboee@exam.mu.ac.in
5	The Director, Board of Students Development, dsd@mu.ac.in DSW direcotr@dsw.mu.ac.in
6	The Director, Department of Information & Communication Technology, director.dict@mu.ac.in

AC – 20/5/2025
Item No. – 6.51 (N)

As Per NEP 2020

University of Mumbai

Syllabus for Major Vertical – 1, 4 & 6		
Name of the Programme – B.E. Electrical & Computer Engineering		
Faculty of Engineering		
Board of Studies in Electrical Engineering		
U.G. Second Year Programme	**Exit Degree**	**U.G. Diploma in Electrical & Computer Engineering**
Semester		**III**
From the Academic Year		**2025-26**

University of Mumbai

(As per NEP 2020)

Sr. No.	Heading	Particulars
1	**Title of program** **O:**_____	**B.E. Electrical & Computer Engineering**
2	**Exit Degree**	**U.G. Diploma in Electrical & Computer Engineering**
3	**Scheme of Examination** **R:** _____	NEP 40% Internal 60% External, Semester End Examination Individual Passing in Internal and External Examination
4	**Standards of Passing** **R:** _____	**40%**
5	**Credit Structure** **R. TEU-680C** **R. TEU-680D**	**Attached herewith**
6	**Semesters**	**Sem. III**
7	**Program Academic Level**	**5.00**
8	**Pattern**	**Semester**
9	**Status**	**New**
10	**To be implemented from Academic Year**	**2025-26**

Sd/-	**Sd/-**	**Sd/-**
Dr. B. R. Patil	**Dr. Deven Shah**	**Prof. Shivram S. Garje**
BoS-Coordinator-Electrical Engineering	**Associate Dean**	**Dean**
Faculty of Technology	**Faculty of Science & Technology**	**Faculty of Science & Technology**

Preamble

To meet the challenge of ensuring excellence and NEP 2020 policy in engineering education, the issue of quality needs to be addressed, debated, and taken forward systematically. Accreditation is the principal means of quality assurance in higher education. The major emphasis of the accreditation process is to measure the outcomes of the program that is being accredited. In line with this Faculty of Science and Technology (in particular Engineering) of the University of Mumbai has taken the lead in incorporating the philosophy of NEP 2020 education in the process of curriculum development.

The second-year engineering course is a core training program to impart scientific and logical thinking training to learners in general, with a choice of course selection from the program core course, multidisciplinary minor, and vocational skill-enhanced course. Simultaneously, the objectives of NEP 2020 demand nurturing the core program and skills required for the 'Electrical & Computer Engineering Branch' of engineering in the learner. Keeping this in view, a pool of courses is offered in Core Courses covering fundamentals required to understand core and modern engineering practices and emerging trends in technology. Considering the shift in pedagogy and the convenience of a stress-free learning process, a choice-based subject pool is offered in the coursework under the heads of 'Electrical & Computer Engineering' in Engineering for open electives and multidisciplinary minor courses in the third and fourth semesters. Essentially, to give a glimpse of trends in the industry under vocational and enhanced skill practices, the pool is offered to nurture and develop creative skills in contemporary industrial practices. Criteria met in the structure is the opportunity for learners to choose the course of their interest in all disciplines.

Program Core Course Cover 'Electrical & Computer Engineering' core courses. Also, OE and MDM where a pool of subjects are given for selection. Considering the present scenario, diverse choices need to be made available to fulfill the expectation of a learner to aspire for a career in the field of current trends of Technology and interdisciplinary research. Ability enhancement can be achieved in Undergraduate training by giving an objective viewpoint to the learning process and transitioning a learner from a rote learner to a creative professional. for the purpose Design Thinking is introduced in the First Semester to orient a journey learner to become a skilled professional. Considering the NEP-2020 structure of award of Certificate & Diploma at multiple exit-point pools of Vocational skills is arranged for giving exposure to the current Industry practices.

The faculty resolved that course objectives and course outcomes are to be clearly defined for every course so that all faculty members in affiliated higher education institutes understand the depth and approach of the course to be taught, which will enhance the learner's learning process. NEP 2020 grading system enables a much-required shift in focus from teacher-centric to continuous-based learner-centric education since the workload estimated is based on the investment of time in learning and not in teaching. It also focuses on continuous evaluation, which will enhance the quality of education. Credit assignment for courses is based on a 15-week teaching-learning process for NEP 2020, however, the content of courses is to be taught in 12-13 weeks, and the remaining 2-3 weeks are to be utilized for revision, tutorial, guest lectures, coverage of content beyond the syllabus, etc.

There was a concern that in the present system, the second-year syllabus must not be heavily loaded to the learner and it is of utmost importance that the learner entering into the second year of an engineering course should feel at ease by lowering the burden of syllabus and credits. This is necessary for a learner to get accustomed to the new environment of a college and to create a bond between the teacher and the learner. The present curriculum will be implemented for the Second Year of Engineering from the academic year 2025-26. Subsequently, this system will be carried forward for Third Year and Final Year Engineering in the academic years 2026-27, and 2027-28, respectively.

Sd/-	**Sd/-**	**Sd/-**
Dr. B. R. Patil	**Dr. Deven Shah**	**Prof. Shivram S. Garje**
BoS-Coordinator-Electrical Engineering	**Associate Dean**	**Dean**
Faculty of Technology	**Faculty of Science & Technology**	**Faculty of Science & Technology**

Under Graduate Diploma in Engineering- Electrical & Computer Engineering.

Credit Structure (Sem. III & IV)

	R. TEU-680C								
Level	**Semester**	**Major**		**Minor**	**OE**	**VSC, SEC (VSEC)**	**AEC, VEC, IKS**	**OJT, FP, CEP, CC,RP**	**Cum. Cr. / Sem.**	**Degree/ Cum. Cr.**
		Mandatory	**Electives**							
5.0	III	PCC301:3 PCC302:3 PCC303:3 PCC304:3 PCL301: 1 PCL302:1	--	--	OE:2	--	VEC: 2 HSL: 2	CEP: 2	22	**UG Diploma 45**
	R. TEU-680D									
	IV	PCC401:3 PCC402:3 PCC403:3 PCL401:1 PCL402:1	--	MDM: 4	OE:2	VSEC:2	VEC: 2 EEM:2	--	23	
	Cum Cr.	25	--	4	4	2	*2+2+2+2*	2	45	

Exit option: Award of UG Diploma in Major and MDM with 90 credits and additional 4 credits core **one** theory subject with 3 credits and **one** lab with 1 credit from one third year from where they want to take Exit degree. Along with theory and practical course student must compulsorily do internship for **one month or 160 hours** which internship is equal to 4 credits.

[Abbreviation - OE – Open Electives, VSC – Vocation Skill Course, SEC – Skill Enhancement Course, (VSEC), AEC – Ability Enhancement Course, VEC – Value Education Course, IKS – Indian Knowledge System, OJT – on Job Training, FP – Field Project, CEP – Continuing Education Program, CC – Co-Curricular, RP – Research Project]

S.E. ‘Electrical & Computer Engineering’ Scheme

Program Structure for Second Year of 'Electrical & Computer Engineering'

UNIVERSITY OF MUMBAI (With Effect from 2025-2026)

SEMESTER III

Course Code	Course Description	Teaching Scheme (Contact Hours/Week)			Credit Assigned			
		Theory	Practical	Tutorial	Theory	Tutorial	Practical	Total Credits
2213111	Engineering Mathematics for Electrical Circuits	2	--	1	2	1	--	3
2213112	Electrical Machines - I	3	–	--	3	--	--	3
2213113	Electronics Engineering	3	--	--	3	--	--	3
2213114	Measurements and Measuring Instruments	3	--	--	3	--	--	3
OEC301	Open Elective	2#	--	--	2	--	--	2
2213115	Electrical Machines & Measurements Lab.	--	2	--	--	--	1	1
2213116	Electronics Engineering Lab.	--	2	--	--	--	1	1
2213611	Mini Project (Group Project)	--	2*+2	--	--	--	2	2
2993511	Entrepreneurship Development	--	2*+2	---	--	--	2	2
2993512	Environmental Science	--	2*+2	--	--	--	2	2
Total		**13**	**16**	**01**	**13**	**01**	**08**	**22**

* Two hours of practical class to be conducted for full class as demo/discussion.

Theory / Tutorial 1 credit for 1 hour and Practical 1 credit for 2 hours

Institute shall offer a course for Open Elective from Science/Commerce/Management stream bucket provided by the University of Mumbai.

Course Code	Course Description	Examination Scheme							
		Internal Assessment Test (IAT)			End Sem. Exam Marks	End Sem. Exam Duration (Hrs)	Term Work (TW)	Oral /Pract.	Total
		IAT-I	IAT-II	Total (IAT-I) + IAT-II)					
2213111	Engineering Mathematics for Electrical Circuits	20	20	40	60	2	25	--	125
2213112	Electrical Machines - I	20	20	40	60	2	--	--	100
2213113	Electronics Engineering	20	20	40	60	2	--	--	100
2213114	Measurements and Measuring Instruments	20	20	40	60	2	--	--	100
OEC301	Open Elective	20	20	40	60	2	--	--	100
2213115	Electrical Machines & Measurements Lab.	--	--	--	--	--	25	25	50
2213116	Electronics Engineering Lab.	--	--	--	--	--	25	25	50
2213611	Mini Project (Group Project)	--	--	--	--	--	50	25	75
2993511	Entrepreneurship Development	--	--	--	--	--	50	--	50
2993512	Environmental Science	--	--	--	--	--	50	--	50
Total		**100**	**100**	**200**	**300**	**10**	**225**	**75**	**800**

Sem. - III

Vertical – 1
Major

Course Code	Course Name	Teaching Scheme (Contact Hours/Week)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2213111	Engineering Mathematics for Electrical Circuits	2	--	1	2	-	1	3

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Exam. Duration (in Hrs.)	Total Marks
		Internal Assessment Test (IAT)			End Sem. Exam			
		IAT-I	IAT-II	IAT-I + IAT-II (Total)				
2213111	Engineering Mathematics for Electrical Circuits	20	20	40	60	25	2	125

Course Objectives: The course is aimed

1. To familiarize with the Laplace Transform, Inverse Laplace Transform of various functions, and its applications.
2. To familiarize the concept of complex variables, C-R equations, harmonic functions, its conjugate and mapping in complex plane
3. To understand the application of Engineering Mathematics to analyze, design, and optimize electrical systems.
4. To use mathematical models to predict how electrical systems will perform under different conditions, allowing designing efficient and reliable technologies in power systems.

Course Outcomes: At the end of the course, students will be able to

1. Apply the concept of Laplace transform and inverse Laplace transform to solve the real integrals in engineering problems.
2. Find orthogonal trajectories and analytic function by using basic concepts of complex variables.
3. Apply Linear Algebra and Complex number operation for the analysis of electrical circuits.
4. Analys electric circuit behavior through matrix operations
5. Analyze the effect of switching conditions on electrical networks using differential equations
6. Analyze time-domain signals and circuit responses using Laplace Transform.

Detailed Syllabus:

Sr. No.	Name of Module	Detailed Content	Hours	CO Mapping
I	Laplace Transform	Definition of Laplace transform and Laplace Transform (L) of Standard Functions like e^{at}, $sin(at)$, $cos(at)$, $sinh\,(at)$, $cosh\,(at)$ and t^n, $n \geq 0$. Properties of Laplace Transform: Linearity, First Shifting theorem, Second Shifting Theorem, change of scale	**5**	CO1

		Property, multiplication by *t*, Division by *t*, Laplace Transform of derivatives and integrals (Properties without proof). Evaluation of integrals by using Laplace Transformation. Inverse Laplace Transform, Linearity property, use of standard formulae to find inverse Laplace Transform, finding Inverse Laplace transform using derivatives. Inverse Laplace transform by Partial fractions.		
II	Function of Complex Variables	Analytic function, necessary and sufficient conditions for *f(z)* to be analytic (without proof). Cauchy-Riemann equations in cartesian coordinates (without proof). Milne-Thomson method to determine analytic function *f(z)* when real part (*u*) or Imaginary part (*v*).	**4**	CO2
III	Application of Linear Algebra and Complex Variables to circuit analysis:	With DC Dependent Sources: Mesh analysis, Super mesh analysis, Nodal analysis, Super node analysis. Superposition theorem, Thevenin's theorems, Norton's theorem, Maximum power transfer theorem; With AC Sources: Magnetic coupling, Mesh analysis, Nodal analysis, Superposition theorem, Thevenin's theorems, Norton's theorem, Maximum power transfer theorem and Reciprocity theorem.	**5**	CO3
IV	Analysis of Electric circuit behavior through matrix operations	Graph Theory and Network Topology: Introduction, Graph of network, Tree, Co-tree, Loop incidence matrix, Cut set matrix, Tie set matrix and Loop current matrix, Number of possible tree of a graph, Analysis of network equilibrium equation and Principle of duality.	4	CO4
V	Application of Differential Equations for analyzing circuits with capacitors and inductors	Solution of first and second order differential equations for series and parallel R-L, R-C, R-L-C circuits, initial and final conditions in network elements, forced and free response, time constants, steady state and transient state response.	**4**	CO5
VI	Electrical Circuit Analysis Using Laplace Transforms	The Laplace transform and its application in electrical circuit analysis, transient and steady state response to step, ramp and impulse signals.	**4**	CO6

Note: Numerical should be covered in Tutorials.

Text Books:

1. Advanced Engineering Mathematics, H.K. Das, S. Chand, Publications
2. Higher Engineering Mathematics, B. V. Ramana, Tata Mc-Graw Hill Publication
3. Advanced Engineering Mathematics, R. K. Jain and S. R. K. Iyengar, Narosa publication
4. W H Hayt, S M Durbin, J E Kemmerly, 'Engineering Circuit Analysis', 7th Edition, Tata McGraw-Hill Education
5. M. E. Van Valkenburg, Network Analysis, 3rd Edition, PHI Learning.

6. D. Roy Choudhury, Networks and Systems, 2nd Edition, New Age International.
7. M. E. Van Valkenburg, Linear Circuits, Prentice Hall.

References:

1. Advanced Engineering Mathematics, Wylie and Barret, Tata Mc-Graw Hil
2. Theory and Problems of Fourier Analysis with applications to BVP, Murray Spiegel, Schaum's Outline Series
3. F. F. Kuo, " Network Analysis and Synthesis", John Wiley and sons.
4. N Balabanian and T.A. Bickart, "Linear Network Theory: Analysis, Properties, Design and Synthesis", Matrix Publishers, Inc.
5. C. L.Wadhwa, "Network Analysis and Synthesis", New Age international.
6. B. Somanathan Nair, "Network Analysis and Synthesis", Elsevier Publications
7. Higher Engineering Mathematics, Dr. B. S. Grewal, Khanna Publication

Online References:

Sr. No.	Website Name
1.	NPTEL/ Swayam Course: Basic Electric Circuits By Prof. Ankush Sharma (IIT Kanpur); https://swayam.gov.in/nd1_noc19_ee36/preview
2.	NPTEL/ Swayam Course: Basic Electrical Circuits by Prof. Nagendra Krishnapura (IIT Madras); https://archive.nptel.ac.in/noc/courses/noc20/SEM2/noc20-ee64/

Term Work:

- Term work: Term work consists of minimum eight tutorials (at least one on each module) and Assignments (min. 2).
- The distribution of the term work shall be as follows:
  Tutorials: 15 marks
  Assignments: 05 marks
  Attendance (Theory and Tutorial) :05 marks

The final certification and acceptance of term-work ensures the minimum passing in the Term Work.

Internal Assessment (IA):

- IA will consist of Two Compulsory Internal Assessment Tests (Each Test of 20 Marks). Approximately 40% to 50% of syllabus content must be covered in First IA Test and remaining 40% to 50% of syllabus content must be covered in Second IA Test

Question Paper Format:

- Question Paper will comprise of a total of **six questions each carrying 15 marks. Q.1** will be **compulsory** and should **cover maximum contents of the syllabus.**
- **Remaining questions** will be **mixed in nature** [part (a) and part (b) of each question must be from different modules. For example, if Q.2 has part (a) from Module 3 then part (b) must be from any other Module randomly selected from all the modules].
- A total of **four questions** needs to be answered.

Course Code	Course Name	Teaching Scheme (Contact Hours/Week)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2213112	Electrical Machines -I	**3**	**--**	**-**	**3**	**-**	**-**	**3**

Course Code	Course Name	Examination Scheme					
		Theory Marks				Exam. Duration (in Hrs.)	Total Marks
		Internal Assessment (IAT)			End Sem. Exam.		
		IAT-I	IAT-II	IAT-I + IAT-II (Total)			
2213112	Electrical Machines -I	20	20	40	60	2	100

Course Objectives: This course is aimed

1. To understand the performance parameters of DC machines.
2. To illustrate the working principle and evaluate the performance of single-phase transformers under different operating conditions.
3. To analyze various connections and performance characteristics of three-phase transformers.
4. To demonstrate the working principle and assess the performance of three-phase induction motors under various operating conditions.
5. To examine different starting methods and speed control techniques for three-phase induction motors.
6. Understand the working of single-phase induction motors.

Course Outcomes: At the end of the course, students will be able to

1) Understand the significance of the DC machines performance parameters.
2) Illustrate working principle and performance of single-phase transformer under different operating conditions.
3) Analyze connections and performance of three phase transformers under various conditions.
4) Demonstrate working principle and evaluate the performance of three phase induction motor under various operating conditions.
5) Exemplify various starting methods and speed control of three phase induction motor.
6) Understand the working of single-phase induction motors.

Detailed Syllabus:

Sr. No.	Name of Module	Detailed Content	Hours	CO Mapping
I	DC Machines	Review of working principles of DC machines, concept of back EMF and torque equations,	08	CO1

		Commutation, Armature reaction, Types of DC machines, Characteristics of DC machines (speed – torque and performance), Necessity of starter, Speed control methods, Losses and efficiency, Swinburne's, Hopkinson's tests.		
II	Single phase Transformer	Review of working principle, EMF equation and Equivalent Circuit, Phasor diagram (Resistive, Inductive and capacitive load), voltage regulation, Losses and Efficiency, Condition for Maximum Efficiency, Parallel Operation: No load Operation, On load Operation: - Equal and Unequal Voltage Operation, OC and SC test, Sumpner's Test	08	CO2
III	Three Phase Transformer	Principle of operation, Connections and Phasor groups, Parallel operation, Harmonics in three phase transformers, Suppression of harmonics, Oscillating neutral phenomenon, Open delta or V - connection, Scott Connection.	07	CO3
IV	Three Phase Induction Motor:	Review of Constructional details and Principle of operation, Slip, Rotor emf and frequency, current and power, Power stages, Phasor diagram, Equivalent circuit, Torque-speed characteristics, Losses and efficiency, No load and blocked rotor test, Circle diagram	08	CO4
V	Starting and Speed control of Three Phase Induction Motor:	Need of starter, Types of starters: Direct On Line (DOL) starter, Rotor resistance starter, Autotransformer and Star delta starters, Speed control: Voltage control, Frequency control, Pole changing method, V/f control.	4	CO5
VI	Single phase Induction Motor:	Principle of operation, Double field revolving theory, Equivalent circuit of single-phase induction motor, starting methods; Split phase starting- Resistance spilt phase, capacitor split phase, capacitor start and run, shaded pole starting	4	CO6

Text Books:

- Bimbhra, P. S. Electric Machinery. New Delhi: Khanna Publishers.
- Bimbhra, P. S. Generalized Machine Theory. New Delhi: Khanna Publishers.
- Husain, Ashfaq. Electric Machines. New Delhi: Dhanpat Rai & Co.
- Mehta, V. K., & Mehta, Rohit. Principles of Electrical Machines. New Delhi: S. Chand Publishing.

Reference Books:

- Say, M. G. Performance and Design of Alternating Current Machines. New Delhi: CBS Publishers & Distributors.
- Fitzgerald, A. E., Kingsley, C., & Umans, S. D. Electric Machinery. New Delhi: Tata McGraw-Hill.

Online References:

Sr. No.	Website Name
1.	https://archive.nptel.ac.in/courses/108/102/108102146/
2.	https://nptel.ac.in/courses/108105155

Assessment:

Internal Assessment (IA):

- IA will consist of Two Compulsory Internal Assessment Tests (each of 20 Marks). Approximately 40% to 50% of syllabus content must be covered in First IA Test and remaining 40% to 50% of syllabus content must be covered in Second IA Test

➢ **Question paper format**

- Question Paper will comprise of a total of **six questions each carrying 15 marks. Q.1** will be **compulsory** and should **cover maximum contents of the syllabus**
- **Remaining questions** will be **mixed in nature** [part (a) and part (b) of each question must be from different modules. For example, if Q.2 has part (a) from Module 3 then part (b) must be from any other Module randomly selected from all the modules]
- A total of **four questions** needs to be answered

Course Code	Course Name	Teaching Scheme (Contact Hours/Week)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2213113	Electronics Engineering	3	--	-	3	-	-	3

Course Code	Course Name	Examination Scheme					
		Theory Marks					
		Internal Assessment (IAT)			End Sem. Exam.	Exam. Duration (in Hrs.)	Total Marks
		IAT-I	IAT-II	IAT-I + IAT-II (Total)			
2213113	Electronics Engineering	20	20	40	60	2	100

Course Objectives: The course is aimed to

1. To impart a foundational understanding of digital logic principles, including number systems, Boolean algebra, and combinational circuit design.
2. To enable students to analyze, design, and implement basic sequential circuits and understand the fundamental principles of memory elements.
3. To familiarize the student with the principle of operation, analysis and design of junction diode, BJT Transistors and FET.
4. Analyze and design analog circuits using semiconductor devices.
5. Understand the basics of microcontrollers.
6. Write assembly language and C programs for microcontrollers to perform various tasks.

Course Outcomes: At the end of the course, students will be able to

1. Perform number system conversions, simplify Boolean expressions using K-maps, and design combinational circuits for various applications.
2. Explain the operation of flip-flops, design basic sequential circuits like shift registers, and describe the functionality of semiconductor memories.
3. Understand the fundamental principles of Analog electronics.
4. To study and design Op Amp Circuits.
5. Describe the concept of microcontroller architectures.
6. Understand the architectural features of MCS-51 variants and select a suitable microcontroller to suit the application.

Detailed Syllabus:

Sr. No.	Name of Module	Detailed Content	Hours	CO Mapping
---	Prerequisite	Understanding of basic electronic components, circuits, and concepts (e.g., voltage, current, resistance, capacitance).	---	---

I	Introduction To Digital Logic	Number Systems and Conversions: Binary, Hexadecimal, Octal, and Decimal conversions. Basic binary arithmetic (addition, subtraction). Logic Gates and Boolean Algebra: AND, OR, NOT, NAND, NOR, XOR gates. Boolean algebra laws and simplification techniques (De Morgan's theorems). Karnaugh Maps (K-maps): Simplification of Boolean expressions using K-maps (up to 4 variables).	6	CO1
II	Combinational, Sequential Circuits & Memory Elements	Combinational Circuit Design: Adders (half, full), Subtractors, Multiplexers (as Universal Logic Modules - ULM), Demultiplexers. Flip-Flops: SR, JK, D, and T flip-flops. Basics of Synchronous & Asynchronous counters. Memory Basics: ROM (types: Mask ROM, PROM, EPROM, EEPROM) and RAM (types: SRAM, DRAM), their basic operation, and differences.	7	CO2
III	Semiconductor Devices	P-N junction diodes: characteristics and applications, Zener diodes: operation and voltage regulation, Light Emitting Diodes (LEDs): construction and characteristics, Opto isolators. BJT structure and operation, BJT characteristics and regions of operation, Junction Field Effect Transistors (JFETs): Construction, operation and device characteristics. V-I relationship and transconductance. Metal-Oxide-Semiconductor FETs (MOSFETs): types, operation, and characteristics.	6	CO3
IV	OP-Amp & Voltage Regulators	Block diagram of Op-amp Ideal Op-Amp characteristics, Inverting and non-inverting configurations, Voltage regulators: Design of adjustable voltage regulators	5	CO4
V	Fundamentals of Microcontroller	Introduction to 8-bit microcontrollers- Basic differences and similarities between Microprocessor and Microcontroller-Types of various architectures; Harvard and Von-Neumann, RSIC and CSIC Concept of pipelining-Selection of microcontrollers, variants of MCS-51 family and their features.	8	CO5
VI	Architecture And Assembly Language Of 8051	8051 microcontroller hardware: Oscillator and Clock, Role of PC and DPTR, Flags and PSW, CPU registers, Internal RAM and RAM organization, Internal Memory, Special Function Registers, I/O pins, ports and circuits, 8051 Addressing modes, Instruction set: Classification, syntax and function of instructions, example programs.	7	CO6

Textbooks:

1. Giovanni Saggio, ―Principles of Analog Electronics‖, CRC Press, Taylor & Francis Group, 2014
2. Jacob. Millman, Christos C.Halkias and Sathyabrata Jit, ―Electronic Devices and Circuits‖, Tata McGraw Hill Publishing Limited, New Delhi, 2010.
3. David A. Bell., ―Electronic Devices and Circuits, Oxford University Press., 5th Edition, 2008
4. Morris Mano, M., Digital Design, Prentice Hall of India (P) Ltd., New Delhi, 2016.

References:

1. Anand Kumar, "Fundamentals of Digital Circuits", Prentice Hall India, 2016
2. R. P. Jain, "Modern Digital Electronics" Tata McGraw Hill Education, 2009
3. Donald A. Neamen, "Semiconductor Physics and Devices" Tata McGraw Hill, Third Edition
4. RL Boylestad and Louis Nashelsky, "Electronic Devices and Circuits" Pentice Hall, Second Edition
5. David Bell, "Electronic Devices and Circuits", Oxford, Fifth Edition

Online References:

Sr. No.	Website Name
1.	NPTEL Course: https://archive.nptel.ac.in/courses/108/101/108101091/

Assessment:

Internal Assessment (IA):

- IA will consist of Two Compulsory Internal Assessment Tests (each of 20 Marks). Approximately 40% to 50% of syllabus content must be covered in First IA Test and remaining 40% to 50% of syllabus content must be covered in Second IA Test

➢ **Question paper format**

- Question Paper will comprise of a total of **six questions each carrying 15 marks. Q.1** will be **compulsory** and should **cover maximum contents of the syllabus**
- **Remaining questions** will be **mixed in nature** [part (a) and part (b) of each question must be from different modules. For example, if Q.2 has part (a) from Module 3 then part (b) must be from any other Module randomly selected from all the modules]
- A total of **four questions** needs to be answered

Course Code	Course Name	Teaching Scheme (Contact Hours/Week)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2213114	Measurements and Measuring Instruments	3	--	-	3	-	-	3

Course Code	Course Name	Examination Scheme					
		Theory Marks				Exam Duration (in Hrs.)	Total Marks
		Internal Assessment (IAT)			End Sem. Exam		
		IAT-I	IAT-II	IAT-I + IAT-II (Total)			
2213114	Measurements and Measuring Instruments	20	20	40	60	2	100

Course Objectives: The course is aimed to

1. To Understand the fundamental principles of measurement and the working of various measuring instruments.
2. To Explain the construction, working, and applications of different types of analog measuring instruments for electrical quantity measurement.
3. To Demonstrate the procedure for balancing AC and DC bridges for determining unknown electrical parameters.
4. To Analyze the working and characteristics of various transducers used for measuring non-electrical quantities.
5. To Compare different types of digital measuring instruments and their significance in electrical measurements.
6. To Evaluate the performance of signal conditioning circuits and advanced measuring instruments for accurate measurement and data acquisition.

Course Outcomes: At the end of the course, students will be able to

1. Comprehend the principles of measurement and various measuring instruments.
2. Describe various types of analog measuring instruments for measurement of electrical quantities.
3. Apply the concepts of balancing of AC, DC Bridges for measurement of unknown electrical parameters.
4. Illustrate the working of various types of transducers for measurement of non-electrical quantities.
5. Describe various types of digital measuring instruments for the measurement of electrical quantities.
6. Illustrate various Signal conditioning and advanced measuring instruments

Detailed Syllabus:

Sr. No.	Name of Module	Detailed Content	Hours	CO Mapping
	Prerequisite	Basic potentiometer circuit, Moving Coil and Moving Iron instruments.		
I	Philosophy of Measurement	Introduction to measurement, Methods of measurement, Classification of instruments, Types of errors in measurement, standardization and calibration of instruments.	**04**	**CO1**
II	Analog Measurement of Electrical Quantities	***Analog Instruments:*** Classifications of analog instruments, Introduction to operating, controlling and damping torque, Extension of range of ammeter and voltmeter, Electro-dynamometer type wattmeter, Electro-dynamometer type Power factor meter. ***Instrument Transformer:*** Current Transformer (CT), Types of CT (measuring and protection), Potential Transformer (PT), Applications in the extension of instrument range, Clamp on ammeter, High voltage potential transformer.	**07**	**CO2**
III	AC-DC Bridges and Potentiometer	***AC, DC Bridges:*** Measurement of resistance: Wheatstone, Kelvins Double, and Megger, earth resistance measurement by earth tester Measurement of inductance and capacitance: Maxwell's bridge, Schering bridge, ***Potentiometer Applications:*** Calibration of ammeter, voltmeter and wattmeter using potentiometer	**07**	**CO3**
IV	Measurement of Non-electrical Quantities	***Transducers:*** definition, Resistive, capacitive & inductive transducers, Optical and digital transducers 1] Measurement of temperature: Thermocouple, Thermistor, Resistance Temperature Detector (RTD) 2] Measurement of displacement: Linear and angular displacement using potentiometer, LVDT 3] Measurement of pressure: Piezoelectric, strain-gauge, capacitive and inductive type 4] Measurement of speed: Inductive and Hall Effect sensor	**07**	**CO4**
V	Digital Measurement of Electrical Quantities	Generalized Block diagram of digital instruments, comparison of digital over analog instruments, Resolution & sensitivity of digital meters, Digital voltmeter (Ramp type), ammeter, multi-meter, Digital frequency meter, Digital Tachometer, Digital Megger, and Digital Storage Oscilloscope, Current sensor (ACS712), Voltage sensor (B25)	**07**	**CO5**
VI	Signal Conditioning and Advanced Measuring Instruments	Basics of AC, DC signal conditioning circuits, Block diagram of Data Acquisition System, Basics of Power analyzer, harmonic analyzer, Signal generator, Q-meter, Introduction to Smart sensor, IoT Based Measuring Instrument, , smart energy meter	**07**	**CO6**

Text Books:

1. A.K. Sawhney, "A Course in Electrical and Electronic Measurements and Instrumentation," Dhanpat Rai Publications.
2. J.B. Gupta, "A Course in Electrical and Electronic Measurements & Instrumentation," Katson Publications.
3. H. S. Kalsi, “Electronic Instrumentation”, Third Edition, Tata McGraw Hill
4. R. K. Rajput, "Electrical and Electronic Measurement and Instrumentation," S. Chand Publications.

References:

1. E.W. Golding & F.C. Widdis, "Electrical Measurements and Measuring Instruments," Wheeler Publishing.
2. Helfric and Cooper, “Modern Electronic Instrumentation and Measurement” Techniques, PHI
3. Sabrie Soloman, “Sensors Handbook”, Second Edition, McGraw Hill
4. Ramon Pallaá S-Areny and J. G. Webster, “Sensors And Signal Conditioning”, Second Edition, John Wiley & Sons, Inc.

Online References:

Sr. No.	Website Name
1.	Electrical Measurement and Electronic instruments, By Prof. Avishek Chatterjee, IIT Kharagpur, weblink: https://onlinecourses.nptel.ac.in/noc24_ee117/preview

Assessment:

Internal Assessment (IA):

- IA will consist of Two Compulsory Internal Assessment Tests (each of 20 Marks). Approximately 40% to 50% of syllabus content must be covered in First IA Test and remaining 40% to 50% of syllabus content must be covered in Second IA Test

➢ **Question paper format**

- Question Paper will comprise of a total of **six questions each carrying 15 marks. Q.1** will be **compulsory** and should **cover maximum contents of the syllabus**
- **Remaining questions** will be **mixed in nature** [part (a) and part (b) of each question must be from different modules. For example, if Q.2 has part (a) from Module 3 then part (b) must be from any other Module randomly selected from all the modules]
- A total of **four questions** needs to be answered

Course Code	Course Name	Teaching Scheme (Contact Hours/Week)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2213115	Electrical Machines and Measurement Lab	-	2	-	-	1	-	1

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Practical/ Oral	Total Marks
		Internal assessment (IAT)			End Sem. Exam.			
		IAT-I	IAT-II	IAT-I +IAT-II (Total)				
2213115	Electrical Machines and Measurement Lab	--	--	--	--	25	25	50

Lab Objectives: The lab course is aimed to

1. To demonstrate the measurement of resistance, including insulation resistance, using appropriate bridge techniques.
2. To apply AC bridge methods for accurate measurement of passive electrical components.
3. To experiment with various sensors and transducers for measuring physical parameters.
4. Provide hands-on experience in the operation, testing, and analysis of DC machines, transformers, and induction motors.
5. Analyze the characteristics, performance, and efficiency of electrical machines through experimental methods.
6. Develop practical skills in conducting standard tests for parameter evaluation and troubleshooting electrical machines.

Lab Outcomes: At the end of the lab course, students will be able to

1. Measure medium, low, and high and insulation resistance using appropriate bridge techniques.
2. Demonstrate/perform the use of AC bridges for measurements of passive electrical components.
3. Analyse various sensors, transducers for measurement of various physical parameters.
4. Operate and test DC machines, transformers, and induction motors using appropriate laboratory instruments and techniques.
5. Analyze and interpret experimental data to determine the characteristics, performance, and efficiency of electrical machines.
6. Perform standard testing procedures such as open circuit, short circuit, load tests, and efficiency evaluation for DC machines, transformers, and induction motors.

Detailed Syllabus:

The Syllabus is same as the Theory Courses “Electrical Machines-I (Course Code: 2213112)” and

"Measurements & Measuring Instruments (Course Code: 2213114).

Online Resources:

Sr. No.	Website Name
1.	https://www.vlab.co.in/broad-area-electrical-engineering
2.	https://elms-iitr.vlabs.ac.in/
3.	https://asnm-iitkgp.vlabs.ac.in/
4.	https://sl-coep.vlabs.ac.in/

List of Experiments:

- At least 4 experiments, each from a list for measurement and electrical machines must be conducted.
- At least 1 assignment, each from a list of assignments for measurement and electrical machines must be conducted.

Sr No	**List of Experiments for Measurement**	Hrs
1.	Measurement of the low/medium resistance using bridge.	2
2.	Measurement of High resistance and insulation resistance using Megger	2
3.	Measurement of inductance using Maxwells bridge. (or Hay's/ Anderson's)	2
4.	Measurement of capacitance using Schering's bridge	2
5.	Measurement of R/L/C using a bridge technique as well as LCR meter.	2
6.	Current Measurement using Shunt, CT, and Hall Sensor.	2
7.	Characterize the temperature sensor RTD.	2
8.	Measurement of temperature using Thermistor or thermocouple	2
9.	Linear displacement measurement using LVDT	2
10.	Measurement of Pressure using Pressure transducer (strain gauge etc.)	2

Sr No	**List of Assignments / Tutorials for Measurement Lab**	Hrs
01	Assignment based on measurement of non-Electrical quantities using sensors & transducers	2
02	Assignment based on smart sensing and smart metering.	2

Sr No	**List of Experiments for Electrical Machines**	**Hrs**
1.	Load test on DC shunt motor/ DC compound motor/ DC series motor.	2
2.	Speed control of DC motors.	2

3.	Swinburne's test on DC motor.	2
4.	Hopkinson's test on DC motor.	2
5.	Sumpner's test on single phase transformer	2
6.	Open circuit and short circuit test on single phase /three phase transformers	2
7.	Parallel operation of transformers.	2
8.	Scott connection of transformer.	2
9.	No load and Blocked rotor test on three phase induction motor. (Determination of equivalent circuit parameters)	2
10.	Performance analysis of three phase induction motor using circle diagram.	2
11.	No load and block rotor test on single phase induction motor.	2
12.	Load test on single phase induction motor.	2

Sr No	List of Assignments / Tutorials for Electrical Machines	Hrs
01	Open circuit and load characteristics of DC shunt generator.	2
02	Study of transformer connections.	2
03	Study of different types of induction motor starters.	2
04	Study of speed control methods used for induction motors.	2

Assessment:

Term Work: Term Work shall consist of at least 8 practicals' based on the above list. Also, Term work Journal must include at least 2 assignments.

Term Work Marks: 25 Marks (Total marks) = 15 Marks (Experiment) + 5 Marks (Assignments) + 5 Marks (Attendance)

Practical & Oral Exam: An Oral & Practical examination will be held based on the above syllabus.

Course Code	Course Name	Teaching Scheme (Contact Hours/Week)				Credits Assigned			
		Theory		Pract.	Tut.	Theory	Pract.	Tut.	Total
2213116	Electronics Engineering Lab	-		2	-	-	1	-	1

Course Code	Course Name	Examination Scheme						
		Theory Marks						
		Internal assessment (IAT)			End Sem. Exam.	Term Work	Practical/ Oral	Total Marks
		IAT-I	IAT-II	IAT-I +IAT-II (Total)				
2213116	Electronics Engineering Lab	--	--	--	--	25	25	50

Lab Objectives: The Lab course is aimed to

1. Familiarize with basic digital logic gates (AND, OR, NOT, NAND, NOR, XOR, XNOR).
2. Understand the basics of sequential circuits, including latches, flip-flops, and counters.
3. Understand the characteristics of semiconductor devices, including diodes, bipolar junction transistors (BJTs), and field-effect transistors (FETs).
4. Understand the basics of OP-amp, including its characteristics, applications, and limitations.
5. Understand the architecture of the 8051 Microcontroller,
6. Learn 8051 assembly language programming, including data types, operators, control structures, and functions

Lab Outcomes: At the end of the Lab course, students will be able to

1. Understand the basics of digital logic, including binary numbers, logic gates, and Boolean algebra.
2. Analyze and design sequential circuits using logic gates, flip-flops, and counters
3. Design and optimize electronic circuits using semiconductor devices, including amplifiers, switches, and rectifiers.
4. Analyze and solve problems related to OP-amp and voltage regulators, including circuit design, performance measurement, and troubleshooting.
5. Analyze and solve problems using 8051 programming, including debugging and troubleshooting.
6. Design and develop 8051-based systems, including hardware and software components.

Detailed Syllabus:

The Syllabus is same as the Theory Course "Electronics Engineering (Course Code: 2213113)".

List of Experiments:

Expt. No	Name of the Experiments	Hours	LO Mapping
1	Verification of Truth Table of Logic Gates	2	LO1
2	SOP and POS Minimization (different problem statement for each group)	2	LO1
3	Constructing Flip-Flops Using All NAND Gates	2	LO1
4	Diode characteristics and applications (Clampers and Rectifiers)	2	LO2
5	BJT and FET characteristics & analysis	2	LO3
6	Design of adjustable Voltage regulator based on LM317.	2	LO4
7	Use of programming tools Keil Compiler to perform addition and subtraction program using direct and indirect addressing	2	LO5

8	Use of programming tools Keil Compiler to perform multiplication and division programs using direct and indirect addressing.	**2**	LO5
9	Use of programming tools Keil Compiler to perform Largest and Smallest Number.	**2**	LO5
10	Write an assembly language program to find number of odds and number of evens from the array of ten numbers stored in internal RAM 20H onwards.	**2**	LO6
11	Use of programming tools Keil Compiler to perform Interfacing of Stepper Motor.	**2**	LO6

Assessment:

Term Work: Term Work shall consist of at least 8 practicals based on the above list. Also, Term work Journal must include at least 2 assignments/tutorials.

Term Work Marks: 25 Marks
(Total Marks) = 15 Marks (Experiment) + 5 Marks (Assignments) +5 Marks (Attendance)

Practical & Oral Exam.: A Practical & Oral examination will be held based on the above syllabus.

Vertical – 6

Course Code	Course Name	Teaching Scheme (Contact Hours/Week)		Credits assigned		
2213611	Mini Project (Group Project)	Theory	Pract. / Tut.	Theory	Pract./Tut.	Total
			2*+2	--	2	2

Course	Course Name	Examination Scheme

Code		Theory					Term Work	Prese-ntation /Oral Exam.	Total Marks
		Internal Assessment			End Sem. Exam.	Exam. Duration (in Hrs.)			
		Test 1	Test 2	Total					
2213611	Mini Project (Group Project)	-	-	-	-	-	50	25	75

Note: * Two hours of practical class to be conducted for full class as demo/discussion/theory.

Lab Objective:

1. To encourage students to identify real-world engineering problems and propose solutions.
2. To develop research and analytical skills through literature review and need assessment.
3. To promote teamwork, leadership, and project management skills.
4. To enhance practical knowledge of electrical components, circuit design, and fabrication.
5. To foster innovation, entrepreneurship, and societal impact.
6. To address societal challenges by working on projects related to sustainability, energy efficiency, and smart technologies.

Lab Outcome:

1. Identify and analyze engineering problems by applying fundamental concepts and systematically evaluating possible solutions.
2. Design and implement engineering solutions by developing, fabricating, and testing circuits or electrical systems that integrate theoretical concepts into real-world applications.
3. Perform testing, debugging, and evaluation of engineering projects using appropriate tools and techniques to validate performance.
4. Demonstrate teamwork and project management skills by working effectively in groups, applying project planning methodologies, and managing resources efficiently.
5. Communicate engineering concepts and findings effectively through structured technical documentation, project reports, and oral presentations.
6. Apply ethical and professional responsibilities in engineering project execution by considering societal and environmental impacts while adhering to professional standards.

A. General Guidelines for Mini Project Execution

1. Formation of Student Groups
   - Students shall form groups of 3 to 4 members.
   - Each group should have a faculty supervisor for guidance.
   - Groups must remain fixed throughout the semester.

2. Selection of Project Topic
   - Students should conduct a survey and identify a problem statement.
   - The problem should be approved by the faculty supervisor or an internal departmental committee.
   - The problem statement should be relevant to electrical engineering and align with industry trends.
   - Preference should be given to projects that impact society positively, such as solutions for rural electrification, renewable energy deployment, or community-based smart systems.

3. Implementation Plan
   - Students shall prepare a Gantt Chart/PERT/CPM chart covering weekly activities.

- Each group must maintain a logbook to record weekly progress, supervisor inputs, and troubleshooting notes.
- Students should conduct literature surveys and use reference materials such as application notes, research papers, and technical datasheets.

4. Design and Fabrication Guidelines
   - Initial design and testing should be conducted using software tools such as LTspice, Eagle, Multisim, or OrCAD.
   - Students should prepare electrical/mechanical drawings, schematics, and layouts before starting fabrication.
   - The prototype should be implemented using general-purpose PCB/Bakelite boards; breadboards should be avoided.
   - If required, a PCB design should be developed and fabricated in the lab.

5. Devices, Components, and Systems to be Used
   - Passive components: Resistors, capacitors, inductors, etc.
   - Semiconductor devices: Diodes, transistors (BJT, MOSFET, IGBT), voltage regulators, logic gates, op-amps, etc.
   - Embedded systems: General-purpose microcontrollers (Arduino, Raspberry Pi, PIC, etc.).
   - Electrical Machines: DC motors, AC motors, relays, transformers, etc.
   - Sensors and actuators: Temperature, proximity, motion sensors, etc.

6. Testing and Debugging
   - Students should use lab equipment such as power supplies, multimeters, oscilloscopes (CRO, DSO), function generators, etc.
   - Problems encountered during testing must be recorded and analyzed in the project logbook.
   - The final working model should be validated against the initial problem statement.

B. Suggested Application Domains

Students are encouraged to choose topics from the following domains or propose innovative ideas:

1. Home/Office Automation
2. Renewable Energy Systems
3. Energy Conservation & Storage
4. Battery Charging and Protection
5. Electrical System Protection
6. Lighting Control & Smart Grids
7. Wireless Power Transfer
8. Electrical Components Testing & Measurement
9. Non-Conventional Energy Generation
10. Robotics & Automation
11. Smart City Solutions (IoT-based systems)
12. Biomedical and Health Monitoring Systems
13. Community Engagement Solutions (e.g., smart public lighting, solar-powered rural electrification, accessible technology for differently-abled individuals)

Note: Faculty can offer any other project based on the syllabus which will help students to understand the topic/concept and implement it for real-life application.

C. Assessment and Evaluation Criteria

1. Continuous Evaluation (Term Work) - 50 Marks

The Mini Project progress will be evaluated through at least two reviews per semester:

- Logbook & Supervisor Marks: 20 Marks
- Review Committee Evaluation: 20 Marks
- Quality of Project Report: 10 Marks

2. Final Presentation & Oral Examination - 25 Marks

Students will present their project, including:

- Problem Statement and Clarity
- Innovativeness of the Solution
- Cost-effectiveness and Societal Impact
- Functionality of the Working Model
- Effective Use of Engineering Norms & Standards
- Individual Contribution and Teamwork
- Clarity in Written and Oral Communication

D. Report Submission Guidelines

- The final report must follow the University of Mumbai format.
- It should include an abstract, literature review, methodology, implementation details, results, and conclusions.
- Students should be encouraged to publish their project work in student conferences or competitions.

Reference Books:

1. P. Horowitz and W. Hill, *The Art of Electronics*, 3rd Edition, Cambridge University Press, 2015.
2. R. S. Khandpur, *Printed Circuit Board*, McGraw-Hill Education, 2005.
3. Simon Monk, *Hacking Electronics: Learning Arduino and Raspberry Pi*, McGraw-Hill Education, 2017.

Software Tools:

1. **LTspice** – Analog circuit simulation
2. **Eagle** – PCB design
3. **OrCAD** – Circuit design and analysis
4. **Multisim** – Virtual electronics lab
5. **Tinkercad** – Online circuit design and simulation
6. **Arduino IDE** – Embedded programming
7. **Raspbian OS** – Raspberry Pi development

Online Resources:

1. Electronics For You
2. Circuit Digest
3. Electronics Hub
4. GitHub

Reference Books:

1. K B. Bhatia, Fundamentals of Maintenance of Electrical Equipments, Khanna Publications
2. BIS SP 30:National Electrical Code
3. Electricity Act 2003

Online Resources:

1. https://www.falstad.com/circuit/
2. https://www.autodesk.com/education/edu-software/overview?sorting=featured&p%20age=1
3. https://www.ti.com/tool/TINA-TI
4. https://www.proficad.com/
5. https://www.kicad.org/

Assessment:

Term Work shall consist of at least 10 to 12 practicals' based on the above syllabus. Also, student need to submit a capstone project based on the above syllabus.
Journal: 15 marks
Experiment: 20 marks
Attendance: 5 marks
Capstone Project: 10 marks

Final Presentation & Practical/Oral Examination: 25 Marks

Students will present their project, including:

- Problem Statement and Clarity
- Innovativeness of the Solution
- Cost-effectiveness and Societal Impact
- Functionality of the Working Model
- Effective Use of Engineering Norms & Standards
- Individual Contribution and Teamwork
- Clarity in Written and Oral Communication

Letter Grades and Grade Points:

Semester GPA/ Programme CGPA Semester/ Programme	% of Marks	Alpha-Sign/ Letter Grade Result	Grading Point
9.00 - 10.00	90.0 – 100	O (Outstanding)	10
8.00 - < 9.00	80.0 - < 90.0	A+ (Excellent)	9
7.00 - < 8.00	70.0 - < 80.0	A (Very Good)	8
6.00 - < 7.00	60.0 - < 70.0	B+ (Good)	7
5.50 - < 6.00	55.0 - < 60.0	B (Above Average)	6
5.00 - < 5.50	50.0 - < 55.0	C (Average)	5
4.00 - < 5.00	40.0 - < 50.0	P (Pass)	4

Below 4.00	Below 40.0	F (Fail)	0
Ab (Absent)	-	Ab (Absent)	0

Sd/-

Dr. B. R. Patil
BoS-Coordinator- Electrical Engineering
Faculty of Technology

Sd/-

Dr. Deven Shah
Associate Dean
Faculty of Science & Technology

Sd/-

Prof. Shivram S. Garje
Dean
Faculty of Science & Technology